

**ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY  
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**

Work Shop on MAT LAB & Simulink during 27<sup>th</sup> to 28th Feb 2020

**Workshop proposal report**

The following are the Budget/Expenditures for the Work Shop on MAT LAB & Simulink during 27<sup>th</sup> to 28th Feb 2020

1. Decoration committee	-----	5000-00
2. Food committee (Lunch, Coffee, Biscuits, Evening snacks)---	-----	15,000-00
3. Publicity committee (Banner & things)	-----	3000-00
4. Certificates & Photo	-----	5000-00
5. Purchase of stationery (File, pad, pen, Bokke, etc)	-----	3000-00
6. Resource person & guests remuneration---	-----	5000-00
7. Miscellaneous expenses	---	6000-00
	-----	
	Total	42,000-00

(For rupees forty two thousand only )

*Podhtha B.*

Course Instructor's Signature



HOD's Signature

**Dr. G. R. Veerendra, M.E., Ph.D.**

Prof. and Head

Dept. of Electrical & Electronics Engg.

A.I.T., Chikkamagaluru-577102

Karnataka - INDIA



**ADICHNUCHANAGIRI INSTITUTE OF TECHNOLOGY**  
**DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**  
**JYOTHINAGAR, CHIKMAGALUR - 577 102**

(Affiliated to V.T.U, Belagavi Recognized by A.I.C.T.E, New Delhi)

(Accredited by N.B.A, New Delhi, ISO 9001: 2008 Certified)

Email: [ait\\_eee@yahoo.com](mailto:ait_eee@yahoo.com) , [gr\\_veerendra@rediffmail.com](mailto:gr_veerendra@rediffmail.com), Ph: +919448945198 office: 08262-220623,.

Tel Fax: 08262-220063



Ref:No.AIT/A2/ /2019-20

Date: 20-02-2020

From:

Dr. G.R.Veerendra,  
Professor & HOD,  
Department of E &E Engineering,  
A.I.T.  
CHIKKAMAGALURU-577102

To:

Bharat  
Asst.Professor,  
Department of E &C Engineering,  
A.I.T.,  
Chikkamagaluru-577102

Sir,

**SUB:** Invitation as a Resource person for MAT LAB & Simulink work-shop.

With reference to the above mentioned subject, we the department of Electrical & Electronics Engineering organizing two day's work-shop on MAT LAB & Simulink. Hence I cordially invite you as a Resource person for inauguration of above work-shop on 27-02-2020 & 28-02-2020. in Dept. of E&E,AIT Chikkamagaluru.

Kindly accept our invitation & oblige.

Thanking You

*Post to B*

*Yours faithfully*

**Dr. G. R. Veerendra, M.E., Ph.D.**  
**Prof. and Head**  
Dept. of Electrical & Electronics Engg.  
A.I.T., Chikkamagaluru-577102  
Karnataka - INDIA

# Matlab & Simulink Workshop REGISTRATION FORM

(Use photocopies for additional participants)

His Holiness Paramapoojya Jagadguru

Sri Sri Sri Dr. Nirmalanandanatha Maha Swamiji

Pontiff, Sri Adichunchanagiri Mahasamsthana Math.

|| ११११११११११११ ||  
Sri Adichunchanagiri Shiksha Trust (R.)

Matlab & Simulink Workshop

Name in block letters: .....

Sri Sri Gunanatha Swamiji  
Sri Adichunchanagiri Mahasamsthana Math, Sringeri Branch

With the divine blessing of

His holiness

Parama Poojya Jagadguru Padmahushana

Sri Sri Sri Dr. Balagangadharanatha Maha Swamiji

and

His holiness Parama Poojya Jagadguru

Sri Sri Sri Dr. Nirmalanandanatha Maha Swamiji

Name of Institution/Organization: .....

Organizing Chair  
Dr. C. K. Subbaraya, Director  
Dr. C. T. Jayadeva, Principal

Convener

Dr. Veerendra G. R

Head, Dept. OF E & E Engineering

Co-Convener

Mr. T. R. Narasimhe Gowda, Associate Prof.

Mrs. Poshita B, Assistant Prof.

Organizing committee

Mr. T. R. Narasimhe Gowda,

Associate Prof.

Mr. Satyanarayana Rao R. D., Assistant Prof.

Mr. Vasantha kumara T. M., Assistant Prof.

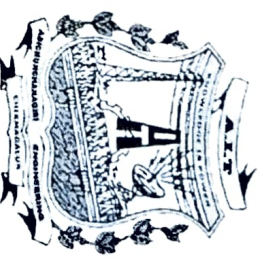
Mr. Joysun D'souza, Assistant Prof.

Mrs. Nandini K. K., Assistant Prof.

Mrs. Poshita B, Assistant Prof.

Mr. Sridhar N. H., Assistant Prof.

Mr. Vinaya Kumara M. B., Assistant Prof.



27 & 28<sup>th</sup> Feb-2020

Department of

Electrical and Electronics Engineering

Adichunchanagiri Institute of Technology,

Chikkamagaluru - 577102, Karnataka, India

DD should be in favour of "The  
Principal, AIT", payable at Chikkamagaluru

accommodation required ?

es No

Signature: Poshita B.  
Signature and Seal of  
**Dr. G. R. Veerendra, M.E., Ph.D**  
Prof. and Head

Dept. of Electrical & Electronics Engg.  
A.I.T., Chikkamagaluru-577102  
Karnataka - INDIA

Chikmagalur Institute of Technology (AIT) was established in 1980 at Chikmagalur with the blessings of revered Jagadguru Padmabhushana Sri Sri Sri Dr. Balagangadharanatha Maha Swamiji Jagadguru of Sri Adichunchanagiri Maha Samsthana Math.

The Institute has highly qualified and experienced faculty with reputation in their areas of specialization. well equipped laboratories, seminar halls with projectors, sufficient books and periodicals in the library. Hygienic canteen facility with hostels for both girls and boys, a huge amphitheatre for cultural exposure and fleet of buses for transportation.

The Institute is providing good training for students to excel in academics as well as in industries and aiming towards 100% placements to give a better future for students. A.I.T is functioning with the motto of Prosperity through Technology and is continuously taking genuine and immense efforts to produce technically competent, socially committed young engineers.

### Location of the college

AIT is located in lush green. Picturesque Salubrious Hilllock of Chikmagalur town, which is famous for Coffee and Tea plantations.

Chikmagalur city is 30 kms from the world-famous sculpture Belur Temple. The renowned temple of Sringeri Sharadamba and Sri Shankaracharya Mutt is just 100 kms away. The college campus is around 65 acres of land with green surroundings. The college has good atmosphere for education. The weather is salubrious and pleasant throughout the year.

### About the department

Department of Electrical & Electronics Engineering was started in the year 1980. The department offers UG program with an intake of 60 students and a PG program in Power System Engineering' with an intake of 24 students. Qualified & well experienced faculty to impart quality technical education. Department is actively involved in R&D activities and industry-institute partnership.

### Vision and Mission

To develop AIT as a centre of excellence and to strive for continuous improvement of technical education and human resource advancement.

To achieve excellence in education, entrepreneurship and innovation by producing engineers with high ethical standards, integrity and credibility.

### About the workshop

Matlab & simulink workshop aims to provide a forum for UG and PG students to present their ideas. The symposium will have plenary lectures followed by parallel sessions. Plenary lectures will be delivered by personalities of repute. It is a platform for the UG and PG students to interact with the well experienced and well versed experts.

### Topics

- INTRODUCTION TO MATLAB
- MATLAB SKILLS
- MATLAB TOOLS
- SIMULINK TOOLS
- HANDS ON MATLAB
- HANDS ON SIMULINK

### Target audience

PG/UG students from Engineering Institutions across the country.

### Registration Fee

- Registration Fee — RS. 500/-

Registration fee is to be sent in the form of DD drawn in favor of "The Principal, AIT" payable at Chikmagalur.

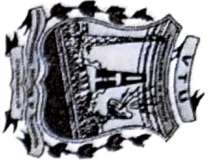
Authors are limited to maximum of two per paper. A photocopy of registration form and DD has to be sent to the email address, [matlab20@gmail.com](mailto:matlab20@gmail.com). The candidate has to submit DD at the time of registration. Accommodation will be provided with prior intimation.

### Important dates

Date of workshop	: 27 & 28 <sup>th</sup> Feb-2020.
Session start from	: 10.00 am
Last date for submission of request	: 25 <sup>th</sup> Feb-2020
Acceptance intimation	: 26 <sup>th</sup> Feb-2020

### Student coordinators:

**Vishnu G Nath**  
Contact no.: 8277437929  
**Gowtham K**  
Contact no.: 8722459796



ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY  
CHIKKAMAGALURU-577102  
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



Work Shop on MATLAB & Simulink during 27<sup>th</sup> to 28<sup>th</sup> Feb 2020

Enrollment Report

S N O	USN	Student Name	Signature
1	4AI17EE001	AHMAED AMEER N	
2	4AI17EE002	ANANYA DIVAKAR H	
3	4AI17EE003	ANVITHA B.M	
4	4AI17EE 004	ASHITA. V	
5	4AI17EE 006	AMRUTHA MS	
6	4AI17EE 007	ANAM KHAISER	
7	4AI17EE 008	BHANUPRIYA. D	
8	4AI17EE 009	BHARAT M.P	
9	4AI17EE 011	BINDUSHREE T.S	
10	4AI17EE 012	CHAITRA V	
11	4AI17EE 013	CHANDANA H.S	
12	4AI17EE 014	CHETANA D NIGALE	
13	4AI17EE 015	CHETANA HK	

15	4AI17EE 017	DEEKSHITH V	
16	4AI17EE 018	GANESH	
17	4AI17EE 020	GANESH PRASAD K.R	
18	4AI17EE 021	GOUTHAM M.S	
19	4AI17EE 023	HARSHA S.K	
20	4AI17EE 024	HARSHITH K.M	
21	4AI17EE 025	ISMAL ZABEULLA	
22	4AI17EE 026	KAVANA H.L	
23	4AI17EE 027	KEERTHI PATEL G.K	
24	4AI17EE 028	MANOJ N.NAIK	
25	4AI17EE 029	MEGHANA H	
26	4AI17EE 030	MEGHANA H.V	
27	4AI17EE 031	MOKSHAN	
28	4AI17EE 032	NISARGA M.S	
29	4AI17EE 033	NIVEDITHA H.K	
30	4AI17EE 034	PRIVANKA C.J	
31	4AI17EE 035	PUNEETH V	
32	4AI17EE 036	SACHIN N	
33	4AI17EE 037	SAMSKRUTHI H.S	
34	4AI17EE 038	SANGEETHA T.R	
35	4AI17EE 039	SANJAY V.S	
36	4AI17EE 040	SHRUTHI A.R	
37	4AI17EE 041	SINCHANAN C	
38	4AI17EE 042	SONU L.S	
39	4AI17EE 043	SUMANTH GOWDA K.H	
		SWATHI H.N	

Sl. No.	Roll No.	Name	Signature
41	4AI17EE 045	SYED YASSER AHMED	
42	4AI17EE 046	TRIVENI H S	
43	4AI17EE 047	THYAGARAJ S/H	
44	4AI17EE 048	VARSHINI B.M	
45	4AI17EE 049	VATSALA K	
46	4AI17EE 050	VIDYA G.K	
47	4AI17EE 051	VINAYAKA BVM	
48	4AI18EE 400	SHREYAS C	
49	4AI18EE 401	ABHISHEK B.S	
50	4AI18EE 402	ABHISHEK B	
51	4AI18EE 403	ARCHANA C.P	
52	4AI18EE 404	ARUNA K.M	
53	4AI18EE 405	BHARATH H	
54	4AI18EE 406	DARSHAN B.K	
55	4AI18EE 407	GOWTHAM H.S	
56	4AI18EE 408	HARISH D	
57	4AI18EE 409	MANOJ KUMAR R	
58	4AI18EE 410	NASURUDDIN M.A	
59	4AI18EE 411	NAVEEN A	
60	4AI18EE 412	NUTHAN NAIK G.S	
61	4AI18EE 413	PRASHANTH C.D	
62	4AI18EE 414	PREMACHANDRA D.S	

Palsetra B

Course Instructor's Signature

HOD's Signature

Dr. G. R. Veerendra, M.E., Ph.D

Prof. and Head

Dept. of Electrical & Electronics Engg.

A.I.T., Chikmagalur.

Karnataka

77102

**ADICHUNCHANGANAGIRI INSTITUTE OF TECHNOLOGY**  
**DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**  
**CHIKKAMAGALURU-577102**

Work Shop on MATLAB & Simulink during 27<sup>th</sup> to 28th Feb 2020

**Note: Session 1: 10-12.30PM Session 2: 2.30-5PM**

**Attendance Report**

S N O	USN	Student Name	27/02/2020		28/02/2020		Signature
			Session-1	Session-2	Session-1	Session-2	
1	4AI17EE001	AHMAED AMEERN	P	P	P	P	<i>Ahmed</i>
2	4AI17EE002	ANANYA DIVAKAR H	P	P	P	P	<i>Ananya</i>
3	4AI17EE003	ANVITHA B.M	P	P	P	P	<i>Anvitha</i>
4	4AI17EE 004	ASHITA V	P	P	P	P	<i>Ashita</i>
5	4AI17EE 006	AMRUTHA MS	P	P	P	P	<i>Amrutha</i>
6	4AI17EE 007	ANAM KHAISER	P	P	P	P	<i>Anam</i>
7	4AI17EE 008	BHANUPRIYA.D	P	P	P	P	<i>Bhanu</i>
8	4AI17EE 009	BHARAT M.P	P	P	P	P	<i>Barat</i>
9	4AI17EE 011	BINDUSHREE T.S	P	P	P	P	<i>Bindu</i>
10	4AI17EE 012	CHAITRA V	P	P	P	P	<i>Chaitra</i>
11	4AI17EE 013	CHANDANA HS	P	P	P	P	<i>Chandana</i>
12	4AI17EE 014	CHETANA D NIGALE	P	P	P	P	<i>Chetana</i>
13	4AI17EE 015	CHETANA HK	P	P	P	P	<i>Chetana</i>
14	4AI17EE 017	DEEKSHITH V	P	P	P	P	<i>Deekshith</i>
15	4AI17EE 018	GANESH	P	P	P	P	<i>Ganesh</i>





Sl. No.	Code	Name	P	P	P	P	P	P	P	Signature
43	4AI17EE 047	THYAGARAJ S H	P	P	P	P	P	P	P	Thyagaraj
44	4AI17EE 048	VARSHINI B M	P	P	P	P	P	P	P	Varshini
45	4AI17EE 049	VATSALAK	P	P	P	P	P	P	P	Vatsala
46	4AI17EE 050	VIDYA G K	P	P	P	P	P	P	P	Vidya
47	4AI17EE 051	VINAYAKA BVM	P	P	P	P	P	P	P	Vinayaka
48	4AI18EE 400	SHREYAS C	P	P	P	P	P	P	P	Shreyas
49	4AI18EE 401	ABHISHEK B S	P	P	P	P	P	P	P	Abhishek
50	4AI18EE 402	ABHISHEK B	P	P	P	P	P	P	P	Abhishek
51	4AI18EE 403	ARCHANA C P	P	P	P	P	P	P	P	Archana
52	4AI18EE 404	ARUNA K M	P	P	P	P	P	P	P	Aruna
53	4AI18EE 405	BHARATH H	P	P	P	P	P	P	P	Bharath
54	4AI18EE 406	DARSHAN B K	P	P	P	P	P	P	P	Darshan
55	4AI18EE 407	GOWTHAM H S	P	P	P	P	P	P	P	Gowtham
56	4AI18EE 408	HARISH D	P	P	P	P	P	P	P	Harish
57	4AI18EE 409	MANOJ KUMAR R	P	P	P	P	P	P	P	Manoj
58	4AI18EE 410	M.A NASURUDDIN	P	P	P	P	P	P	P	M.A Nasuruddin
59	4AI18EE 411	NAVEEN A	P	P	P	P	P	P	P	Naveen
60	4AI18EE 412	NUTHAN NAIK G S	P	P	P	P	P	P	P	Nuthan
61	4AI18EE 413	PRASHANTH C D	P	P	P	P	P	P	P	Prashanth
62	4AI18EE 414	PREETHI B	P	P	P	P	P	P	P	Preethi
		PREMACHANDR A D S	P	P	P	P	P	P	P	Premachandra

Course Instructor's Signature

Poshita B

HOD's Signature  
**DR. G. R. Vengeru, M.E., Ph.D**  
 Prof. and Head  
 Dept. of Electronics & Electronics Engg.  
 Anna University, Chennai-600 025  
 INDIA



|| Jai Sri Gurudev ||

Sri Adichunchanagiri Shikshana Trust (R)

ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY

(Affiliated to VTU, Belagavi and Approved by AICTE, New Delhi)

Chikkamagaluru - 577 102, Karnataka, India.



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

## Two days Workshop on *MATLAB & SIMULINK*

27<sup>th</sup> - 28<sup>th</sup> /02/2020

### FEEDBACK FORM

**Particulars:**

	A	B	C	D
1. workshop objectives were stated clearly and met	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2. Facilities used in this workshop	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3. Course relevance to current trends	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4. Hands on training Provided	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5. Knowledge gained during workshop	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6. The presenter provided adequate time for questions and Answered them satisfactorily	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7. Overall rating of the workshop	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

8. Comments and suggestions (if any):

\_\_\_\_\_

Note: A: Excellent      B: Very Good      C: Good      D: Satisfactory

Thank you for participating in this workshop. Your comments will enable us to do better plan with future workshop and tailor them to meet your needs.

Dr. G. R. Veerendra, M.E., Ph.D  
Prof. and Head  
Dept. of Electrical & Electronics Engg  
A.I.T., Chikkamagaluru-577102  
Karnataka - INDIA



Sri Adichunchanagiri Shikshana Trust  
**Adichunchanagiri Institute of Technology**

Chikkamagaluru - 577102, Karnataka  
(Affiliated to Visvesvaraya Technological University, Belgaovi,  
Approved by AICTE, New Delhi and Recognized by Govt. of Karnataka)  
ACCREDITED BY NAAC, NRA - ISO 9001:2008 CERTIFIED



## **Report on**

Two days work-shop on  
MAT LAB & Simulink

### **Resource Person**

**Bharat**

Assistant Professor

Department of E & C Engineering,

A.I.T, Chikkamagaluru

**Organized by**

**Department of Electrical & Electronics Engineering**

on 27<sup>th</sup> to 28<sup>th</sup> Feb 2020

Department of E&E organized an two day's work-shop on MAT LAB & Simulink for VI Semester B.E Electrical & Electronics students on 27<sup>th</sup> to 28th Feb 2020.

The aim of the two day's work-shop on MAT LAB & Simulink was to introduce recent trends of important aspects of MAT LAB & Simulink. During this work-shop so many aspects of MAT LAB & Simulink with hands on training are presented by Mr. Bharat, Assistant professor, Dept. of E&C, Ait, Chikkamagaluru. He explained the opportunity to explore current topics and practical applications. This course covers Acquiring new skills, getting inspired to work better, enhance proper communication skills.



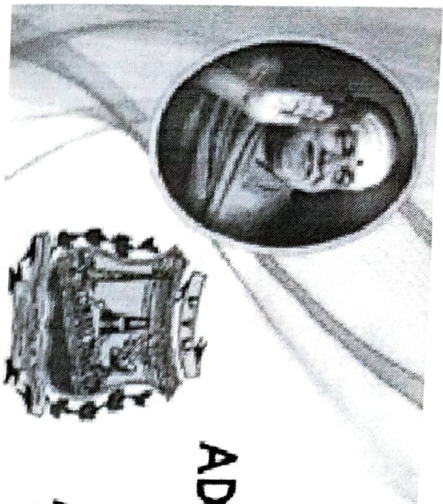
Principal Dr. C.T Jayadeva Chief guests of this work-shop , he spoke regarding importance & implementation of recent technology like MAT LAB & Simulink. Dr G. R Veerendra, Prof & Head, Dr. Anil kumar & Workshop coordinator Poshita B, Asst.prof., sitting in the dias.

#### **Outcome of the Add on course:**

Students have shown lot of interests in the work-shop .They got more knowledge by knowing various aspects of hands on training, current topics and practical applications, acquiring new skills, getting inspired to work better, enhance proper communication skills,. A positive feedback regarding the same was received.

*Poshita B*

**Dr. G. R. Veerendra, M.E., Ph.D**  
**Prof. and Head**  
Dept. of Electrical & Electronics Engg.  
A.I.T., Chikkamagaluru-577102  
Karnataka - INDIA



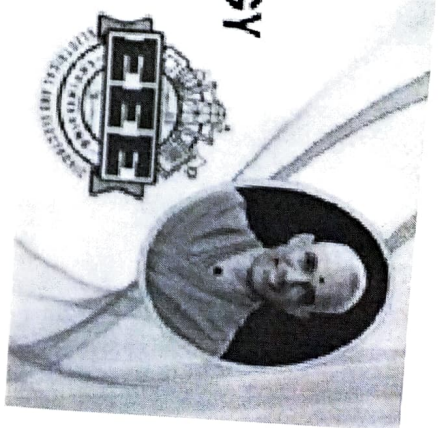
|| Sri Sri Gurudev ||

Sri Adichunchanagiri Shikshana Trust (R)

**ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY**  
**CHIKKAMAGALURU-577 102**

**Department of Electrical & Electronics Engineering**

*Certificate of Participation*



*This is to certify that CHAITRA V has successfully completed the Work Shop on course on "MAT LAB & Simulink " from 27<sup>th</sup> to 28<sup>th</sup> February 2020 ,organized by Department of Electrical and Electronics Engineering.*

*Poshita B*  
Prof. Poshita B  
Staff co-ordinator

Dr. G.R Veerendra

H.O.D  
**Dr. G. R. Veerendra, M.E, Ph.D**  
**Prof. and Head**  
Dept. of Electrical & Electronics Engg.  
A.I.T., Chikkamagaluru-577102  
Karnataka - INDIA

|| Jai Sri Gaurdev ||

Sri Adichunchanagiri Shikshana Trust (R)

**ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY**  
**CHIKKAMAGALURU-577 102**

**Department of Electrical & Electronics Engineering**

*Certificate of Participation*

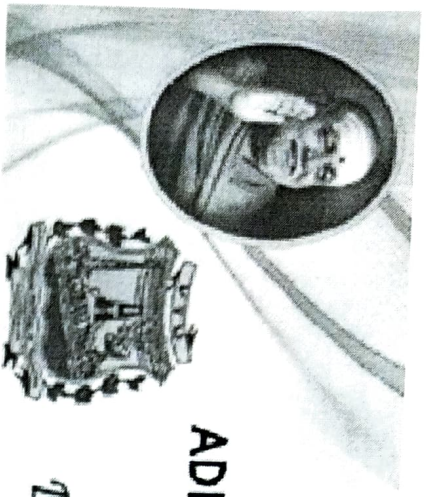
*This is to certify that BINDUSHREE T.S has successfully completed the Work Shop on course on "MAT LAB & Simulink " from 27<sup>th</sup> to 28<sup>th</sup> February 2020, organized by Department of Electrical and Electronics Engineering.*

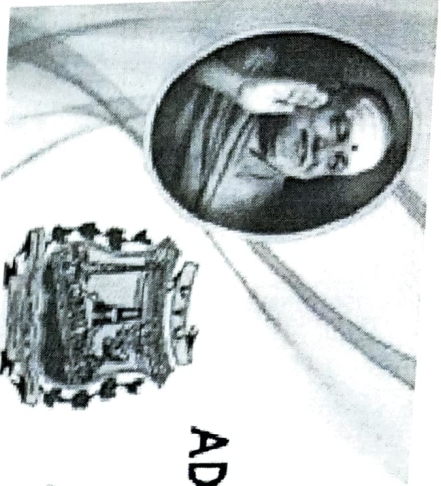
*Poshita B*

Prof. Poshita B  
Staff co-ordinator



Dr. G.R. Veerendra  
H.O.D  
Prof. and Head  
Dept. of Electrical & Electronics Engg  
A.I.T., Chikkamagaluru-577102  
Karnataka - INDIA





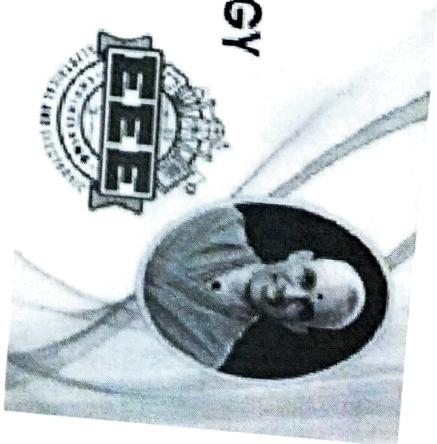
|| Jai Sri Gurudev ||

Sri Adichunchanagiri Shikshana Trust (R)

**ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY**  
**CHIKKAMAGALURU-577 102**

**Department of Electrical & Electronics Engineering**

*Certificate of Participation*



*This is to certify that DEEKSHITH V has successfully completed the Work Shop on course on "MAT LAB & Simulink " from 27<sup>th</sup> to 28<sup>th</sup> Febravary, 2020, organized by Department of Electrical and Electronics Engineering.*

*Poshita B*  
Prof. Poshita B  
Staff co-ordinator

Dr. G.R. Veerandra  
**Dr. G. R. Veerandra, M.E., Ph.D**  
Prof. and Head  
Dept. of Electrical & Electronics Engg.  
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**ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY**  
**DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**

Work Shop on MAT LAB & Simulink during 27<sup>th</sup> to 28th Feb 2020

**Budget/Expenditure Report**


The following are the Budget/Expenditures for the Work Shop on MAT LAB & Simulink during 27<sup>th</sup> to 28th Feb 2020

1. Decoration committee	-----	5000-00
2. Food committee	-----	5000-00
(Lunch, Coffee, Biscuits, Evening snacks)---		15,000-00
3 Publicity committee	-----	2000-00
(Banner & things)		
4 Certificates & Photo	-----	4000-00
5 Purchase of stationary	-----	3000-00
(File, pad, pen, Bokke, etc)		
6 Resource person & guests remuneration---		5000-00
7 Miscellaneous expenses	-----	5000-00
	-----	
<b>Total</b>		<b>39,000-00</b>

(For rupees thirty nine thousand only )

Course Instructor's Signature

Rajeev B

  
HOD's Signature  
**DR. G. R. Veerendra, M.E., Ph.D**  
Prof. and Head  
Dept. of Electrical & Electronics Engg.  
A.I.T., Chikmagalur-577102  
Karnataka - INDIA