



# ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ

ವಿಶ್ವವಿದ್ಯಾಲಯ ಮಂಗಳೂರು ಕರಾವಳಿಯಲ್ಲಿ ಕರ್ನಾಟಕ ಸರ್ಕಾರದಿಂದ ಸ್ಥಾಪಿತವಾದ ರಾಜ್ಯ ವಿಶ್ವವಿದ್ಯಾಲಯ

ಸಂಗಮ ಮಚ್ಚೆ, ಬೆಳಗಾವಿ-590018

## Visvesvaraya Technological University

(The State University of Govt. Karnataka, Established as per VTU Act 1994)

"JnanaSangama" Machhe, Belagavi-590018, www.vtu.ac.in



Dr. A. S. Deshpande B.E., Tech., Ph.D.  
Registrar

Phone: (0831) 2498100  
Fax: (0831) 2405467

Ref. No. VTU/BGM/BOS/2021-22/ 709

Date: 29 APR 2022

### NOTIFICATION

**Subject:** Academic Calendar of IV semester MBA, II semester B.Sc., IV semester B.E./B.Tech., and (revised) VI semester B.E./B.Tech./B. Plan and (revised) I semester B.E./B.Tech./B.Plan/B.Arch. programs of University regarding...

**Reference** Hon'ble Vice-Chancellor's approval dated: 25.04.2022

Academic Calendar of IV semester MBA, II semester B.Sc., IV semester B.E./ B.Tech., (revised) VI semester B.E./B.Tech./B.Plan., and (revised) I semester B.E./B.Tech./ B.Plan./ B.Arch., programs of the University are shown on the 2<sup>nd</sup> page of this notification.

The Principals of Affiliated, Constituent and Autonomous Engineering Colleges are hereby informed to bring the academic calendar to the notice of all concerned.

Sd/-  
REGISTRAR

To,

1. The Principals of all affiliated/ constituent /Autonomous Engineering Colleges under the ambit of VTU Belagavi.
2. The chairperson, Department of Mechanical Engineering /Civil Engineering /Computer Science and Engineering and Business Studies of the University.

Copy to.

1. To the Hon'ble Vice-Chancellor through the secretary to VC, VTU Belagavi for information
2. The Registrar (Evaluation), VTU Belagavi for information.
3. The Regional Directors (I/c) of all the regional offices of VTU for circulation.
4. The Director SMUITI, VTU Belagavi for information and to make arrangements to upload Academic Calendar on the VTU web portal.
5. The Director of Physical Education, VTU Belagavi for information
6. PS to Registrar VTU Belagavi
7. All the concerned Special Officer/s and Caseworker/s of the academic section, VTU, Belagavi

Sd/-  
REGISTRAR

**Academic Calendar for IV sem MBA / IV sem B.E./B.Tech.(Revised) VI sem B.E./B.Tech /B.Plan., (Revised) B.E./B.Tech./B.Arch./B.Plan., and II sem B.Sc. Programs for AY-2021-22**

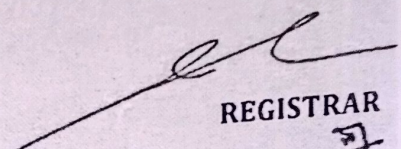
	VI semester B.E./B.Tech. (Revised)	VI semester B.Plan. (Revised)	IV Semester MBA	IV semester B.E./B.Tech	II semester B.Sc.	I sem B.E./B.Tech./ B.Plan/B.Arch (Revised)
Commencement of Semester	04.04.2022	04.04.2022	09.05.2022	16.05.2022	23.05.2022	13.12.2021
Last Working day of Semester	16.07.2022	16.07.2022	20.08.2022	27.08.2022	05.09.2022	10.05.2022
Practical/Viva-Examination	18.07.2022 To 29.07.2022	18.07.2022 To 29.07.2022	---	01.09.2022 To 08.09.2022	06.09.2022 To 09.09.2022	28.05.2022 To 04.06.2022
Theory Examinations	01.08.2022 To 20.08.2022	01.08.2022 To 20.08.2022	22.08.2022 To 14.09.2022	12.09.2022 To 30.09.2022	12.09.2022 To 28.09.2022	12.05.2022 To 27.05.2022
Internship	21.08.2022 To 10.09.2022	21.08.2022 To 10.09.2022	---	---	---	---
Internship Viva-Voce/ Project viva	---	---	---	---	---	---
Summer Project / Professional training / Organization Study	---	---	---	---	---	---
Submission of the report University	---	---	11.07.2022 To 22.07.2022	---	---	---
Commencement of NEXT Semester	19.09.2022	19.09.2022	---	10.10.2022	10.10.2022	06.06.2022

**Please Note:**

- The academic sessions for EVEN semesters should commence from the dates mentioned above.
- All the students of VI semesters B.E./B.Tech. programs have to join the VII semester after completion of their INTERNSHIP during the above-mentioned duration.
- The Institute/Department shall plan to have extra classes to complete the requisite hours of teaching and learning as per the scheme.
- Faculty should conduct additional tutorial classes in blended mode to solve the doubts of the students.

Faculty should conduct additional tutorial classes in blended mode to solve the doubts of the students.

- The faculty/staff shall be available to undertake any work assigned by the university.
- Notification regarding the Calendar of Events relating to the conduction of University Examinations will be issued by the Registrar (Evaluation) from time to time.
- Academic Calendar may be modified based on guidelines/directions issued in the future by MHRD/UGC/AICTE/State Government.
- Academic Calendar is also applicable for Autonomous Colleges. In case any changes are to be effected by Autonomous Colleges in the academic terms and examination schedule, they could do so with the approval of the University.

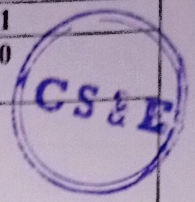
  
REGISTRAR

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||JAI SRI GURUDEV||

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AIT	College Calendar of Events		Format No.	ACD01
			Issue No.	01
			Rev. No.	00
<u>Academic Year : 2021-2022</u>		<u>Semester :Even</u>		
Sl No.	Date	Event	Remarks	
1	4 Apr 2022	Commencement of 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E. classes.		
2	14 Apr 2022	Holiday, Mahaveer Jayanthi, Ambedkar Jayanthi		
3	3 May 2022	Holiday, Basava Jayanthi, Ramzan		
4	21 May 2022	First Test Cycle for 8 <sup>th</sup> semester B.E. Students		
5	23 May 2022	Commencement of 3 <sup>rd</sup> semester B.E. classes.		
6	27 May 2022 to 29 May 2022	First Test Cycle for 6 <sup>th</sup> semester B.E. Students		
7	30 May 2022	Workshop on "Importance of Administrative tasks " for non-teaching staff		
8	6 June 2022	Commencement of 2 <sup>nd</sup> semester B.E. classes.		
9	10 June 2022	Second Test Cycle for 8 <sup>th</sup> semester B.E. Students		
10	14 June 2022	Ethnic Day for 8 <sup>th</sup> semester Students		
11	15 June 2022	Induction Program for first year Students		
12	16 June 2022	Graduation Day for 8 <sup>th</sup> semester Students		
13	17 June 2022 & 18 June 2022	Chunchana-2022		
14	25 June 2022 to 27 June 2022	Second Test Cycle for 6 <sup>th</sup> semester B.E. Students		
15	30 June 2022	Third Test Cycle for 8 <sup>th</sup> semester B.E. Students. Last Working Day for 8 <sup>th</sup> semester B.E. classes.		
16	1 July 2022 to 3 July 2022	First Test Cycle for 4 <sup>th</sup> and 2 <sup>nd</sup> semester B.E. Students		
17	4 July 2022 to 20 July 2022	Theory Examinations for 8 <sup>th</sup> semester B.E. Students		



18	14 July 2022 to 16 July 2022	Third Test Cycle for 6 <sup>th</sup> semester B.E. Students	
19	16 July 2022	Last Working Day for 6 <sup>th</sup> semester B.E. classes.	
20	18 July 2022 to 29 July 2022	Practical Examinations for 6 <sup>th</sup> semester B.E. Students	
21	21 July 2022 to 30 July 2022	Internship viva-voce/Project Viva for 8 <sup>th</sup> Semester Students	
22	31 July 2022 to 2 Aug 2022	Second Test Cycle for 4 <sup>th</sup> and 2 <sup>nd</sup> semester B.E. Students	
23	1 Aug 2022 to 20 Aug 2022	Theory Examinations for 6 <sup>th</sup> semester B.E. Students	
24	9 Aug 2022	Holiday, Muharram	
25	15 Aug 2022	Holiday, Independence Day	
26	27 Aug 2022 to 29 Aug 2022	Third Test Cycle for 4 <sup>th</sup> and 2 <sup>nd</sup> semester B.E. Students	
27	31 Aug 2022	Holiday, Ganesha Chaturthi	
28	1 Sept 2022 to 8 Sept 2022	Practical Examinations for 4 <sup>th</sup> semester B.E. Students	
29	11 Sept 2022 to 29 Sept 2022	Theory Examinations for 2 <sup>nd</sup> semester B.E. Students	
30	12 Sept 2022 to 30 Sept 2022	Theory Examinations for 4 <sup>th</sup> semester B.E. Students	
31	1 Oct 2022 to 10 Oct 2022	Practical Examinations for 2 <sup>nd</sup> semester B.E. Students	

Note: Add any other events like Guest Lecture, National/International Conference, Seminars, etc in individual department calendar of events.

Copy to:

1. All HOD's
2. Placement Officer
3. Establishment Section
4. Dhi Team

*CT Sanjay*

PRINCIPAL  
AIT, Chickmagalur



|| JAI SRI GURUDEV ||

AIT		College Calendar of Events		Format No.	ACD01
				Issue No.	01
				Rev. No.	00
Academic Year :		2019-20	Semester :	Odd	
Sl No	Date	Events			Remark
1	22/07/2019	After availing the vacation all the faculty members reported back to the duty			
2	29/07/2019	Commencement of 3 <sup>rd</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> Semester B.E. Classes.			
3	05/08/2019	Commencement of 1 <sup>st</sup> semester B.E. classes.			
4	05/08/2019	Commencement of Induction Programme for 1 <sup>st</sup> Semester BE Students.			
5	08/08/2019	Commencement of 3 <sup>rd</sup> Semester MBA classes.			
6	12/08/2019	Holiday, Bhakrid.			
7	15/08/2019	Celebration of 73 <sup>rd</sup> Independence day.			
8	16/08/2019 to 17/08/2019	DSP & DSP Architecture course will be conducted for the interested students of 5 <sup>th</sup> & 7 <sup>th</sup> semester E&CE students by Dr Rathna G N, Professor, IISC.			
9	24/08/2019	Technical talk for the students in Electrical and Electronics Engineering Department.			
10	26/08/2019	Commencement of 3 <sup>rd</sup> Semester M.Tech. classes.			
11	02/09/2019	Holiday, Ganesha Festival.			
12	10/09/2019	Holiday, Last day of Moharam.			
13	11/09/2019	Awareness program for girls on health & hygienic in E&CE department by Dr Gowri, MBBS, MD.			
14	13/09/2019 to 15/09/2019	Commencement of First Test Cycle for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students.			
15	19/09/2019	Announcement of 1 <sup>st</sup> Test I.A Marks for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students.			
16	21/09/2019 to 23/09/2019	Commencement of First Test Cycle for 1 <sup>st</sup> semester B.E Students.			
17	21/09/2019	Hands on training on Design of Transformer/MAT Lab for 5th semester students of Electrical and Electronics Engineering.			
18	27/09/2019	Announcement of 1 <sup>st</sup> Test I.A Marks for 1 <sup>st</sup> semester B.E Students.			
29	28/09/2019	Holiday, Mahalaya Amavase.			
20	02/10/2019	Holiday, Gandhi Jayanthi.			
21	07/10/2019	Holiday, Aayudha Pooja.			
22	08/10/2019	Holiday, Vijayadashami.			
23	24/10/2019 to 26/10/2019	Commencement of Second Test Cycle for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students			
24	29/10/2019	Holiday, Deepavali Festival.			

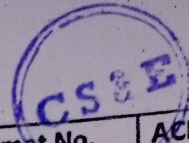
25	30/10/2019	Announcement of 2 <sup>nd</sup> Test I.A Marks for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students.	
26	01/11/2019	Holiday, Kannada Rajyotsava.	
27	07/11/2019 to 09/11/2019	Commencement of Second Test Cycle for 1 <sup>st</sup> semester B.E Students.	
28	14/11/2019	Announcement of 2 <sup>nd</sup> Test I.A Marks for 1 <sup>st</sup> semester B.E Students.	
29	15/11/2019	Holiday, Kanakadasa Jayanthi.	
30	25/11/2019 to 27/11/2019	Commencement of Third Test Cycle for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students	
31	29/11/2019	Announcement of 3 <sup>rd</sup> Test I.A Marks for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students.	
32	30/11/2019	Last working day for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> Semester B.E classes	
33	03/12/2019 to 13/12/2019	Practical Examinations for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> Semester B.E Students.	
34	05/12/2019	Last working day for 3 <sup>rd</sup> Semester MBA classes.	
35	09/12/2019 to 04/01/2020	Theory Examinations for 3 <sup>rd</sup> Semester MBA students.	
36	16/12/2019 to 18/12/2019	Commencement of Third Test Cycle for 1 <sup>st</sup> semester B.E Students.	
37	16/12/2019 to 07/02/2020	Theory Examinations for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> Semester B.E Students.	
38	20/12/2019	Announcement of 3 <sup>rd</sup> Test I.A Marks for 1 <sup>st</sup> semester B.E Students.	
39	21/12/2019	Last working day for 1 <sup>st</sup> semester B.E Students.	
40	23/12/2019	Last working day for 3 <sup>rd</sup> Semester M.Tech. classes.	
41	23/12/2019 to 03/01/2020	Practical Examinations for 1 <sup>st</sup> Semester B.E Students.	
42	25/12/2019	Holiday, Christmas day.	
43	27/12/2019 to 10/01/2020	Theory Examinations for 3 <sup>rd</sup> Semester M.Tech. students.	
44	06/01/2020 to 28/01/2020	Theory Examinations for 1 <sup>st</sup> Semester B.E Students.	
45	12/01/2020 to 19/01/2020	Internship Viva- Voce for 3 <sup>rd</sup> Semester M.Tech. students.	
46	27/01/2020	Commencement of Even Semester for MBA and M.Tech. students.	
47	10/02/2020	Commencement of Even Semester for B.E. students.	

Note: Add any other events like Guest Lecture, National / International Conference, seminars, etc., in individual department calendar of events.

Copy to:

1. All HOD'S
2. Placement Officer
3. Establishment Section
4. Spansos Team.

Dr. C. T. JAYADEVA  
Principal  
B.E., M.Tech., Ph.D.  
Adichunchanagiri Institute of Technology



AIT		College Calendar of Events		Format No.	ACD01
				Issue No.	01
				Rev. No.	00
Academic Year :		2019-20	Semester :	Even	
Sl.No.	Date	Events			Remark
1	27/01/2020	Commencement of 4 <sup>th</sup> Semester M.Tech. classes.			
2	31/01/2020	After availing the vacation all the faculty members reported back to the duty			
3	03/02/2020 to 07/02/2020	FDP on FPGA and Embedded design flow on Zynq using Vivado in Department of Electronics and Communication Engineering.			
4	05/02/2020 to 06/02/2020	Faculty Development Program on "Microcontroller & Embedded Systems" in Department of Information Science & Engineering			
5	07/02/2020	Interdisciplinary Internal Faculty Development Program for faculty of all the Departments in Department of Mechanical Engineering.			
6	10/02/2020	Commencement of 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> , 8 <sup>th</sup> Semester B.E. and 4 <sup>th</sup> semester MBA classes.			
7	10/02/2020 to 20/02/2020	Induction Programme for 2 <sup>nd</sup> Semester B.E. Students.			
8	14/02/2020	Commencement of 2 <sup>nd</sup> Semester MBA classes.			
9	20/02/2020	Technical Talk on "Different Products Of ULTRATECH CEMENT Industry" in Department of Civil Engineering.			
10	21/02/2020	Holiday, Mahashivarathri.			
11	05/03/2020	Commencement of 2 <sup>nd</sup> Semester M.Tech. classes.			
12	20/03/2020 to 21/03/2020	Workshop on "Selenium Test Automation Tool" in Department of Information Science & Engineering.			
13	22/03/2020 to 24/03/2020	First Test Cycle for B.E Students.			
14	25/03/2020	Holiday, Chandramana Ugadhi.			
15	27/03/2020	Work Shop on "AUTO DESK Products in Civil Engineering"			
16	30/03/2020	Announcement of 1 <sup>st</sup> Test I.A Marks for B.E Students.			
17	31/03/2020	Project Exhibition in Department of Mechanical Engineering.			
18	03/04/2020 to 04/04/2020	Fourth National Conference on Emerging trends and Advances in Information technology and 2 <sup>nd</sup> State Level Project Exhibition in Department of Computer Science & Engineering.			
19	05/04/2020	National Conference on "Emerging Trends In Business & Economy" in Department of Management Studies (MBA)			
20	06/04/2020	Holiday, Mahaveera Jayanthi.			
21	10/04/2020	Holiday, Good Friday.			
22	14/04/2020	Holiday, Dr. Ambedkar Jayanthi.			
23	18/04/2020	Seminar on "Cyber Security and Cyber Forensics" in Department of Information Science & Engineering			

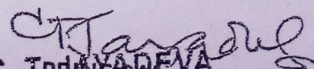


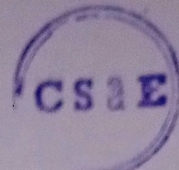
	23/04/2020 to 25/04/2020	Second Test Cycle for B.E Students	
25	30/04/2020	E-belaku National Level Symposium in Department of Electrical & Electronics Engineering.	
26	30/04/2020	Announcement of 2 <sup>nd</sup> Test I.A Marks for B.E Students.	
27	01/05/2020	Holiday, Labours day.	
28	04/05/2020	Awareness program for girls on health & hygienic in Department of Electronics and Communication Engineering.	
29	08/05/2020	One Day National Conference on Recent Advances in Civil Engineering and Technology ( RACET-2020)	
30	20/05/2020	Last working day for 4 <sup>th</sup> Semester M.Tech. classes.	
31	25/05/2020	Holiday, Kutub-A-Ramzan.	
32	26/05/2020 to 28/05/2020	Third Test Cycle for B.E Students	
32	30/05/2020	Announcement of 3 <sup>rd</sup> Test I.A Marks for B.E Students.	
33	01/06/2020	Last working day for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> Semester B.E and 4 <sup>th</sup> semester MBA classes.	
34	03/06/2020 to 11/06/2020	Theory Examinations for 8 <sup>th</sup> Semester B.E and 4 <sup>th</sup> semester M.Tech. students.	
35	03/06/2020 to 13/06/2020	Practical Examinations for 2 <sup>nd</sup> , 4 <sup>th</sup> and 6 <sup>th</sup> Semester B.E Students.	
36	03/06/2020 to 28/06/2020	Theory Examinations for 4 <sup>th</sup> semester MBA students.	
37	05/06/2020	Last working day for 2 <sup>nd</sup> Semester MBA classes.	
38	08/06/2020 to 20/06/2020	Theory Examinations for 2 <sup>nd</sup> Semester MBA students.	
39	15/06/2020 to 20/06/2020	Viva Voce for 8 <sup>th</sup> semester B.E. students.	
40	15/06/2020 to 20/07/2020	Theory Examinations for 2 <sup>nd</sup> , 4 <sup>th</sup> and 6 <sup>th</sup> Semester B.E Students.	
41	22/06/2020	Last working day for 2 <sup>nd</sup> Semester M.Tech. classes.	
42	25/06/2020 to 30/06/2020	Practical Examinations for 2 <sup>nd</sup> Semester M.Tech. students.	
43	01/07/2020 to 11/07/2020	Theory Examinations for 2 <sup>nd</sup> Semester M.Tech. students.	
44	27/07/2020	Commencement of Odd Semester for B.E. and MBA students.	
45	03/08/2020	Commencement of Odd Semester for M.Tech. students.	

Note: Add any other events like Guest Lecture, National / International Conference , seminars, etc., in individual department calendar of events.

Copy to:

1. All HOD'S
2. Placement Officer
3. Establishment Section
4. Spaces Team

  
 Dr. C. T. PRAVEENA  
 B.E. M.Tech. Ph.D.



AIT		College Calendar of Events			Format No.	ACD01
					Issue No.	01
					Rev. No.	00
Academic Year		2018-2019	Semester	EVEN	Date: 01-02-2019	
Sl No	Date	Events				Remark
1	28-12-2018	Commencement of IV Semester M.Tech Classes				
2	15-01-2019	Sankranthi Festival				
3	26-01-2019	Republic Day				
4	01.02.2019	Commencement of IV,VI,VIII Semester B.E Classes				
5	18-02-2019	Commencement of IV Semester MBA Classes				
6	04.02.2019	Traffic Awareness Programme organized by RTO and District Administration, Chikmagalur				
7	05-02-2019	Commencement of Lecture Capture in Higher semester Class rooms				
8	25-02-2019	Commencement of II Semester B.E and MBA Classes				
9	01-03-2019	Commencement of II semester M.Tech Classes				
10	04-03-2019	Maha Shivaratri				
11	14-03-2019 to 16-03-2019	Commencement of First IA Cycle for 4 <sup>th</sup> ,6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students.				
12	21-03-2019	Announcement of 4 <sup>th</sup> ,6 <sup>th</sup> and 8 <sup>th</sup> semester First IA Marks and sending of IA marks to Parents from Spanceos Portal				
13	03-04-2019 to 05-04-2019	Commencement of First IA Cycle for II semester Students				
14	06-04-2019	Yugadi Festival				
15	11-04-2019	Announcement of II Semester First IA Marks and sending of IA'marks to Parents from Spanceos Portal				
16	12-04-2019	National Level Technical Symposium "E-BELAKU19"				
17	13-04-2019	Last working day of IV semester M.tech classes.				
18	15-04-2019	National Conference on Emerging Trends in Mechanical Engineering				
19	17-04-2019	Mahavira Jayanthi				
20	19-04-2019	Good Friday				
21	20-04-2019 to 22-04-2019	Commencement of Second IA Cycle for 4 <sup>th</sup> ,6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students.				
22	25-04-2019	"Graduation Day" for B.E., M.TECH and MBA students.				
23	26 <sup>th</sup> & 27 <sup>th</sup> April 2019	"CHUNCHANA" Cultural Fest-2019				

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24	27-04-2019	Announcement of 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> Semester Second IA Marks and sending of IA marks to Parents from Spanceos Portal	
25	30-04-2019	National Conference in the Department of Electronics and Communication Engineering	
26	01-05-2019	Worker's Day	
27	07-05-2019	Basava Jayanthi/ Akshaya Trithiya	
28	10-05-2019 to 12-05-2019	Commencement of Second IA Cycle for II semester Students	
29	17-05-2019	Announcement of II Semester Second IA Marks and sending of IA marks to Parents from Spanceos Portal	
30	18 <sup>th</sup> , 20 <sup>th</sup> & 21 <sup>st</sup> May 2019	Commencement of Third IA Cycle for 4 <sup>th</sup> and 6 <sup>th</sup> semester B.E Students.	
31	22-05-2019	Announcement of 4 <sup>th</sup> and 6 <sup>th</sup> Semester Third IA Marks and sending of IA marks to Parents from Spanceos Portal.	
32	23-05-2019	Last working day of IV, VI & VIII semester B.E Classes	
33	27-05-2019 to 07.06.2019	VTU Theory Examination of VIII semester B.E. Students	
34	27-05-2019 to 31-05-2019	VTU Theory Examination of IV Semester M.Tech Students	
35	27-05-2019 to 07.06.2019	VTU Practical Exam for IV and VI semester B.E. students	
36	03-06-2019 to 28-06-2019	VTU theory Examination for IV Semester MBA students	
37	10-06-2019 to 16-07-2019	VTU Theory Examination for IV and VI semester B.E. students	
38	13-06-2019 to 15-06-2019	Commencement of Third IA Cycle for II semester B.E. Students	
39	11-06-2019 to 17-06-2019	VTU Project Viva Voce Exam for VIII semester B.E. students	
40	22-07-2019	Commencement of ODD Semester	

Note : Add any other events like Guest Lecturer, National / International Conference , seminars, etc., in individual department calendar of events.

Copy to :

1. All HOD'S
2. Placement Officer
3. Establishment Section
4. Spanceos Team.

*C.T. Jayadev*  
PRINCIPAL

**Dr. C. T. JAYADEVA**

Principal B.E., M.Tech., Ph.D.

Adichunchanagiri Institute of Technology  
CHIKKAMAGALURU-577102



||JAI SRI GURUDEV||

AIT

College Calendar of Events

Format No.

ACD01

Issue No.

01

Rev. No.

00

Academic Year	2018-2019	Semester	ODD	Date :23-07-2018
Date	Events			Remark
22.07.2018	One day awareness program for the aspirants taking competitive exams like UPSC, KPSC, Staff selections, Banking services, etc.			
23-07-2018	After availing the vacation all the faculty members reported back to the duty			
27-07-2018	Principal address all the faculty members of AIT, MBA about odd semester academic activities.			
01-08-2018	Commencement of 3 <sup>rd</sup> , 5 <sup>th</sup> Semesters of B.E. Classes.			
06.08.2018	Commencement of 7 <sup>th</sup> Semester of B.E. Classes & MBA class.			
13.08.2018	Commencement of 1 <sup>st</sup> semester B.E. classes.			
13-08-2018	Commencement of Induction programme for 1 <sup>st</sup> Semester BE Students.			
15.08.2018	Celebration of 72 <sup>nd</sup> Independence day celebration.			
22.08.2018	Holiday, Bhakrid.			
17.09.2018 to 19.09.2018	Commencement of First Test Cycle for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students.			
13.09.2018	Holiday, Ganesha Festival.			
21.09.2018	Holiday, Last day of Moharam.			
02.10.2018	Holiday, Gandhi Jayanthi.			
03.10.2018	Holiday, Mahalaya Amavase.			
10-10-2018	Announcement of 1 <sup>st</sup> Test I.A Marks for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students.			
18.10.2018	Holiday, Aayudha Pooja.			
19.10.2018	Holiday, Vijayadashami.			
24.10.2018	Holiday, Maharshi Valmiki Jayanthi.			
25.10.2018, to 27.10.2018	Commencement of Second Test Cycle for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students and First test cycle for 1 <sup>st</sup> semester B.E Students.			
27-10-2018	Announcement of 2 <sup>nd</sup> Test I.A Marks for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students and First test I.A Marks for 1 <sup>st</sup> semester B.E Students.			
01.11.2018	Holiday, Kannada Rajyotsava.			
06.11.2018	Holiday, Naraka chaturdashi.			
08.11.2018	Holiday, Deepavali Festival.			
15-11-2017	Conduction of National conference in all departments.			

25	16-11-2017	Conduction of National conference In all departments.
26	21.11.2018	Holiday, Ed-Milad Festival.
27	26.11.2018	Holiday, Kanakadasa Jayanthi.
28	27-11-2018 to 29-11-2018	Commencement of Improvement Test Cycle for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students and Commencement of Second Test Cycle for 1 <sup>st</sup> semester B.E Students.
29	27-11-2018	Announcement of Improvement Test I.A Marks for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students and of Second test I.A Marks for 1 <sup>st</sup> semester B.E Students.
30	07.01.2019 to 09.01.2019	Commencement of Improvement Test Cycle for 1 <sup>st</sup> semester B.E Students.
31	08.01.2019	Announcement of Improvement Test I.A Marks for 1 <sup>st</sup> semester B.E Students.
32	30-11-2018	Last working day for 3 <sup>th</sup> & 5 <sup>th</sup> Semester B.E Students.
33	04-12-2018	Last working day for 7 <sup>th</sup> Semester B.E Students.
34	25.12.2018	Holiday, Christmas day.
35	17-01-2018	Last working day for 1 <sup>st</sup> Semester B.E Students.
36	06-12-2019 to 14-12-2019	Practical Examination for 7 <sup>th</sup> semester BE Students.
37	03-12-2018 to 14-12-2018	Practical Examination for 3 <sup>th</sup> & 5 <sup>th</sup> semester BE Students.
38	21-01-2019 to 30-01-2019	Practical Examination for 1 <sup>st</sup> semester BE Student.
39	17-12-2018 to 18-01-2019	Theory Examination for 3 <sup>th</sup> , 5 <sup>th</sup> and 7 <sup>th</sup> semester BE, students.
40	05-12-2018 to 29-12-2018	Theory examinations for 3 <sup>rd</sup> Semesters of M B A Students.
41	03-01-2019 to 16-02-2019	Submission of report to VTU by 08.03.2019 3 <sup>rd</sup> Semesters of M B A Students.
42	01-02-2019	Commencement of EVEN semester's 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester classes.
43	18-02-2019	Commencement of 2 <sup>nd</sup> Semesters M B A classes.
44	25-02-2019	Commencement of 2 <sup>th</sup> semester classes.

Note : Add all the activities done by individual departement and highlight the Non-Working days, Guest Lecturers, Seminars, Test Cycles etc .

Copy to:

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3. Establishment Section
4. vijayalakshmi.a@spaneos.com

*CT Jayaraj*  
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CHIKKAMAGALURU - 577 10.

Revised

-Mats



||JAI SRI GURUDEV||

AIT

College Calendar of Events

Format No.

ACD01

Issue No.

01

Rev. No.

00

Academic Year

2017-18

Semester

EVEN

Date :10-02-2018

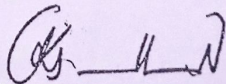
Sl No	Date	Events	Remark
1	08.01.2018	Commencement of 4 <sup>th</sup> Semesters of M.Tech Classes	
2	29-01-2018	After availing the vacation all the faculty members reported back to the duty	
3	31.01.2018	One day work shop on Application of Mathematics in Engineering for all the branches	
4	05-02-2018	Commencement of 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> Semesters of B.E. Classes	
5	01.02.2018	BARC Students Empowerment Programme by Sri Ramachandra Aital	
6	08.02.2018 to 10.02.2018	Three Days Faculty Development Programme on "ARM Cortex M3 Microcotroller" organized by E&C Department.	
7	10.02.2018	Commencement of 2 <sup>nd</sup> semester MBA classes	
8	10.02.2018	One day national work shop on "Chemistry of Nanomaterials and their Engineering Applications"	
9	17-02-2018	Commencement of 2 <sup>nd</sup> , Semesters of M.Tech Classes	
10	10-03-2018 to 12-03-2018	Commencement of First Test Cycle for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students	
11	16-03-2018	Announcement of 1 <sup>st</sup> Test I.A Marks for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students	
12	26-03-2018	Commencement of 4 <sup>th</sup> semester MBA classes	
13	14-04-2018 to 16-04-2018	Commencement of Second Test Cycle for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students	
14	19-04-2018	Announcement of 2 <sup>nd</sup> Test I.A Marks for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students	
15	26-04-2018	Graduation Day for outgoing B.E. M.Tech. and AIMB students.	
16	27-04-2018 & 28-04-2018	"CHUNCHANA" - Cultural Fest - 2018	
17	28-04-2018	Last working day for 4 <sup>th</sup> semester M.Tech Students	

18	12.05.2018		Conduction of National conference in all departments
19	18-05-2018 to 20-05-2018	to	Commencement of Improvement Test Cycle for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students
20	22-05-2018		Announcement of Improvement Test I.A Marks for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students
21	23-05-2018		Last working day for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students
22	28.05-2018 to 06-06-2018		Practical Examination for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> semester B.E Students
23	08.06.2018		Last working day for 2 <sup>nd</sup> semester M.Tech Students
24	11-06-2018 to 16-06-2018		Practical Examination for 2 <sup>nd</sup> semester M.Tech Students
25	28-05-2018 to 02-06-2018		Theory Examination for 4 <sup>th</sup> semester M.Tech, students
26	28.05.2018 to 08.06.2018		Theory Examination for 8 <sup>th</sup> semester B.E., students
27	04-06-2018 to 18-06-2018		Theory examinations for 2 <sup>nd</sup> Semesters of MBA Students
28	18.06.2018 to 30.06.2018		Theory examinations for 2 <sup>nd</sup> Semesters of M.Tech Students
29	11-06-2018 to 16-06-2018		External project Viva-voce for 8 <sup>th</sup> semester students
30	11-06-2018 to 14-07-2018		Theory examinations for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> semester B.E Students
31	16-07-2018 to 08-08-2018		Theory examinations for 4 <sup>th</sup> Semesters of M B A Students
32	13.07.2018		Last working day for 4 <sup>th</sup> semester MBA Students
33	04.06.2018 to 18.06.2018		Theory examinations for 2 <sup>nd</sup> Semesters of MBA Students
34	01-08-2018		Commencement of ODD semester tentatively

**Note :** Add all the activities done by individual department and highlight the Non-Working days, Guest Lecturers, Seminars, Test Cycles etc

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PRINCIPAL

Dr. C. K. SUBBARAYA

Principal

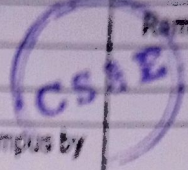
Adichunchanagiri Institute of Technology

Chikmagalur - 577 102

## College Calendar of Events

Format No.	ACC01
Revision No.	01
Flow No.	00
Date	01/07/2017

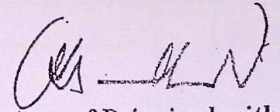
Sl No	Date	Events	Remark
1	7-8-2017	Commencement of Odd semester (3 <sup>rd</sup> , 5 <sup>th</sup> , 7 <sup>th</sup> ) B. E. classes	
2	11-8-2017	Commencement of 1 <sup>st</sup> semester B. E. classes	
3	15-08-2017	71 <sup>st</sup> Independence day celebration & Swachh Kharatha Abhiyan in the campus by students and faculties	
4	18-8-2017	One month HR training for final year students completed with valedictory function	
5	23-08-2017	Commencement of MBA classes	
6	25-08-2017	Holiday for Vianayak Chathurthi	
7	26-08-2017	Linking Holiday	
8	28-08-2017 & 29-08-2017	VTU central zone table tennis tournament	
9	2-9-2017	Holiday for Bakrid	
10	9-9-2017	Rally for river program at BGS block	
11	10-9-2017 to 12-9-2017	Virtual drive, aptitude and verbal and technical training by HR department	
12	11-9-2017	"Digital transformation in AIT software demo program" by Spneos software solution pvt. Ltd., Bangalore	
13	13-9-2017	Campus drive by Wipro	
14	16-9-2017	Engineers Day celebration in association with institution of engineers, Davanagere Chapter	
15	16-9-2017 to 18-9-2017	Commencement of First test cycle for 1 <sup>st</sup> , 3 <sup>rd</sup> , 5 <sup>th</sup> & 7 <sup>th</sup> Semester B.E. students	
16	19-9-2017	Mahalaya Amavasye	
17	21-9-2017	Announcement of First test IA marks for 1 <sup>st</sup> , 3 <sup>rd</sup> , 5 <sup>th</sup> & 7 <sup>th</sup> Semester B.E. students	
18	21-09-2017 & 22-9-2017	NAAC review meeting with Sri Nagalingappa	
19	26-9-2017 & 27-9-2017	B. Capegemini drive	
20	27-9-2017 & 28-9-2017	Mock Accreditation by external experts	
21	29-9-2017	Mahanavami Ayuda Pooja	
22	30-9-2017	Holiday for Vijayadashami	
23	2-10-2017	Gandhi Jayanthi	
24	3-10-2017 & 4-10-2017	Workshop / Seminars at the department level	
25	5-10-2017	Maharishi Valmiki Jayanthi	
26	9-10-2017 to 11-10-2017	NAAC peer Team visit to the college	
27	18-10-2017 & 20-10-2017	Naraka Chathurdashi & Balipadyami, Dipavali	
28	28-10-2017 to 30-10-2017	Commencement of Second test cycle for 1 <sup>st</sup> , 3 <sup>rd</sup> , 5 <sup>th</sup> & 7 <sup>th</sup> Semester B.E. students	
29	1-11-2017	Kannada Rajyotsava	
30	6-11-2017	Kanakadasa Jayanthi	
31	7-11-2017	Announcement of Second test IA marks for 1 <sup>st</sup> , 3 <sup>rd</sup> , 5 <sup>th</sup> & 7 <sup>th</sup> Semester B.E.	



8-11-2017 to 20-11-2017 Commencement of Improvement test cycle 1<sup>st</sup>, 3<sup>rd</sup>, 5<sup>th</sup> & 7<sup>th</sup> Sem Students.



	24-11-2017	Announcement of Improvement test IA marks for 1 <sup>st</sup> , 3 <sup>rd</sup> , 5 <sup>th</sup> & 7 <sup>th</sup> Semester B.E. students	
34	25-11-2017	Last working day of odd (1 <sup>st</sup> to 7 <sup>th</sup> ) semester B. E. students	
35	29-11-2017 to 8-12-2017	Practical examinations for odd (1 <sup>st</sup> to 7 <sup>th</sup> ) semester B E students	
36	11-12-2017 to 30-12-2017	Theory examination for 1 <sup>st</sup> semester B E Students	
37	11-12-2017 to 10-1-2018	Theory examination for 3 <sup>rd</sup> , 5 <sup>th</sup> & 7 <sup>th</sup> semester B E Students	
38	13-12-2017 to 10-1-2018	Theory examination for 3 <sup>rd</sup> semester MBA Students	
39	1-2-2018	Commencement of Even semester classes	
<b>te : All the activities done by individual department and highlight the non working days, Guest lecturers, seminars, test cycles etc.</b>			



Signature of Principal with Date

**Dr. C. K. SUBBARAYA**

Principal

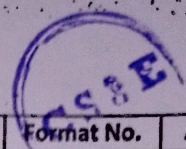
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4. Placement Officer
5. S.W.O.
6. Dean of Academic
7. vijayalakshmi.a@spaneos.com

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AIT

College Calendar of Events

Format No.

ACD01

Issue No.

01

Rev. No.

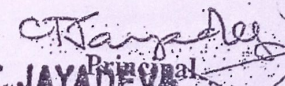
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Academic Year: 2021-2022

Semester: Odd

Sl.No.	Date	Event	Remarks
1.	1 Oct 2021	Commencement of 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E. classes.	
2.	2 Oct 2021	Holiday, GANDHI JAYANTHI	
3.	6 Oct 2021	Holiday, MAHALAYA AMAVASYA	
4.	11 Oct 2021	Technical talk on "Parallel computing" by Dr. Geetha V, Professor, NITK- Surathkal, in the Department of Computer Science and Engineering.	
5.	14 Oct 2021	Holiday, AYUDHA POOJA & MAHA NAVAMI	
6.	15 Oct 2021	Holiday, DUSSEHRA & VIJAYADASHAMI	
7.	18 Oct 2021	Commencement of 3 <sup>rd</sup> semester B.E. classes.	
8.	19 Oct 2021	Holiday, ED MILAD	
9.	20 Oct 2021	Holiday, MAHARSHI VALMIKI JAYANTHI	
10.	1 Nov 2021	Holiday, KANNADA RAJYOTSAVA	
11.	3 Nov 2021	Holiday, NARAKA CHATURDASHI	
12.	5 Nov 2021	Holiday, BALIPADYAMI	
13.	08 Nov 2021	Commencement of 3 <sup>rd</sup> semester MBA classes.	
14.	11 Nov 2021	Commencement of 3 <sup>rd</sup> semester M. Tech. classes.	
15.	19 Nov 2021 to 21 Nov 2021	First Test Cycle for 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students.	
16.	22 Nov 2021	Holiday, KANAKADASA JAYANTHI	
17.	23 Nov 2021	Webinar on "Money Management and Career Opportunities Post Covid Scenario" in P.G. Department of Management Studies.	
18.	27 Nov 2021	Technical Talk on Placements in the Department of Information Science and Engineering.	
19.	04 Dec 2021	Cultural and Technical Fest in the Department of Information Science and Engineering.	
20.	08 Dec 2021 to 10 Dec 2021	First Test Cycle for 3 <sup>rd</sup> semester B.E Students.	
21.	13 Dec 2021	Commencement of 1 <sup>st</sup> semester B.E. classes.	
22.	15 Dec 2021	Technical Talk by Dr. Prakash R, Professor and Head, Department of Electrical and Electronics Engg, Acharya Institute of Technology, Bengaluru, in the Department of Electrical and Electronics Engg.	
23.	18 Dec 2021	Technical Talk on "Application of Artificial Intelligence and Machine Learning in Control Engineering", by Karthikeya Kamath in the Department of Mechanical Engineering.	
24.	24 Dec 2021	Seminar on "Structural Detailing" in the Department of Civil Engineering.	
25.	25 Dec 2021	Holiday, CHRISTMAS	
26.	29 Dec 2021 to 31 Dec 2021	Second Test Cycle for 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students.	
27.	08 Jan 2022	Technical Talk on "Big Data" in the Department of Information Science and Engineering.	
28.	11 Jan 2022 to 13 Jan 2022	Second Test Cycle for 3 <sup>rd</sup> semester B.E Students.	
29.	18 Jan 2022	National Conference in the Department of Information Science and Engineering.	
30.	22 Jan 2022 to 24 Jan 2022	First Test Cycle for 1 <sup>st</sup> semester B.E Students.	
31.	27 Jan 2022 to 29 Jan 2022	Third Test Cycle for 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E Students.	

2.	29 Jan 2022	Seminar on "Concrete mix design" in the Department of Civil Engineering.	
33.	31 Jan 2022	Last Working Day for 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E. classes.	
34.	01 Feb 2022 to 10 Feb 2022	Practical Examination for 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E. students.	
35.	11 Feb 2022 to 25 Mar 2022	Theory Examination for 5 <sup>th</sup> and 7 <sup>th</sup> semester B.E. students.	
36.	14 Feb 2022 to 16 Feb 2022	Third Test Cycle for 3 <sup>rd</sup> semester B.E Students.	
37.	19 Feb 2022	Last Working Day for 3 <sup>rd</sup> semester B.E. classes.	
38.	21 Feb 2022 to 04 Mar 2022	Practical Examination for 3 <sup>rd</sup> semester B.E. students.	
39.	25 Feb 2022 to 27 Feb 2022	Second Test Cycle for 1 <sup>st</sup> semester B.E Students.	
40.	28 Feb 2022	Last Working Day for 3 <sup>rd</sup> semester M.Tech. and MBA classes.	
41.	01 Mar 2022 to 05 Mar 2022	Practical Examination for 3 <sup>rd</sup> semester M.Tech. students.	
42.	02 Mar 2022 to 25 Mar 2022	Theory Examination for 3 <sup>rd</sup> semester MBA students.	
43.	07 Mar 2022 to 25 Mar 2022	Theory Examination for 3 <sup>rd</sup> semester B.E. and M.Tech. students.	
44.	28 Mar 2022 to 30 Mar 2022	Third Test Cycle for 1 <sup>st</sup> semester B.E Students.	
45.	30 Mar 2022	Last Working Day for 1 <sup>st</sup> semester B.E. classes.	
46.	01 Apr 2022 to 08 Apr 2022	Practical Examination for 1 <sup>st</sup> semester B.E. students.	
47.	04 Apr 2022	Commencement of 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E. and 4 <sup>th</sup> semester M.Tech. classes.	
48.	11 Apr 2022	Commencement of 4 <sup>th</sup> semester B.E. classes.	
49.	11 Apr 2022 to 23 Apr 2022	Theory Examination for 1 <sup>st</sup> semester B.E. students.	
50.	25 Apr 2022 to 14 Apr 2022	Internship for 1 <sup>st</sup> semester B.E. students.	
51.	09 May 2022	Commencement of 4 <sup>th</sup> semester MBA classes.	
52.	16 May 2022	Commencement of 2 <sup>nd</sup> semester B.E. classes.	

  
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 CHIKKAMAGALURU-577102

AIT	College Calendar of Events		Format No.	ACD01
			Issue No.	01
			Rev. No.	00
<u>Academic Year : 2021-2022</u>		<u>Semester : Even</u>		
Sl No.	Date	Event	Remarks	
1	4 Apr 2022	Commencement of 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E. classes.		
2	14 Apr 2022	Holiday, Mahaveer Jayanthi, Ambedkar Jayanthi		
3	3 May 2022	Holiday, Basava Jayanthi, Ramzan		
4	21 May 2022	First Test Cycle for 8 <sup>th</sup> semester B.E. Students		
5	23 May 2022	Commencement of 3 <sup>rd</sup> semester B.E. classes.		
6	27 May 2022 to 29 May 2022	First Test Cycle for 6 <sup>th</sup> semester B.E. Students		
7	30 May 2022	Workshop on "Importance of Administrative tasks " for non-teaching staff		
8	6 June 2022	Commencement of 2 <sup>nd</sup> semester B.E. classes.		
9	10 June 2022	Second Test Cycle for 8 <sup>th</sup> semester B.E. Students		
10	14 June 2022	Ethnic Day for 8 <sup>th</sup> semester Students		
11	15 June 2022	Induction Program for first year Students		
12	16 June 2022	Graduation Day for 8 <sup>th</sup> semester Students		
13	17 June 2022 & 18 June 2022	Chunchana-2022		
14	25 June 2022 to 27 June 2022	Second Test Cycle for 6 <sup>th</sup> semester B.E. Students		
15	30 June 2022	Third Test Cycle for 8 <sup>th</sup> semester B.E. Students. Last Working Day for 8 <sup>th</sup> semester B.E. classes.		
16	1 July 2022 to 3 July 2022	First Test Cycle for 4 <sup>th</sup> and 2 <sup>nd</sup> semester B.E. Students		
17	4 July 2022 to 20 July 2022	Theory Examinations for 8 <sup>th</sup> semester B.E. Students		

18	14 July 2022 to 16 July 2022	Third Test Cycle for 6 <sup>th</sup> semester B.E. Students	
19	16 July 2022	Last Working Day for 6 <sup>th</sup> semester B.E. classes.	
20	18 July 2022 to 29 July 2022	Practical Examinations for 6 <sup>th</sup> semester B.E. Students	
21	21 July 2022 to 30 July 2022	Internship viva-voce/Project Viva for 8 <sup>th</sup> Semester Students	
22	31 July 2022 to 2 Aug 2022	Second Test Cycle for 4 <sup>th</sup> and 2 <sup>nd</sup> semester B.E. Students	
23	1 Aug 2022 to 20 Aug 2022	Theory Examinations for 6 <sup>th</sup> semester B.E. Students	
24	9 Aug 2022	Holiday, Muharram	
25	15 Aug 2022	Holiday, Independence Day	
26	27 Aug 2022 to 29 Aug 2022	Third Test Cycle for 4 <sup>th</sup> and 2 <sup>nd</sup> semester B.E. Students	
27	31 Aug 2022	Holiday, Ganesha Chaturthi	
28	1 Sept 2022 to 8 Sept 2022	Practical Examinations for 4 <sup>th</sup> semester B.E. Students	
29	11 Sept 2022 to 29 Sept 2022	Theory Examinations for 2 <sup>nd</sup> semester B.E. Students	
30	12 Sept 2022 to 30 Sept 2022	Theory Examinations for 4 <sup>th</sup> semester B.E. Students	
31	1 Oct 2022 to 10 Oct 2022	Practical Examinations for 2 <sup>nd</sup> semester B.E. Students	

||JAI SRI GURUDEV||



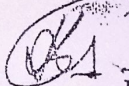
AIT		College Calendar of Events			Format No.	ACD01
					Issue No.	01
					Rev. No.	00
Academic Year		2016-17	Semester	EVEN	Date :13-01-2017	
Sl No	Date	Events				Remark
1	16-01-2017	After availing the vacation all the faculty members reported back to the duty				
2	21-01-2017	Principal address all the faculty members of AIT,BBM,MBA about even semester academic activities .				
3	23-01-2017	Commencement of 2 <sup>nd</sup> , 4 <sup>th</sup> Semesters of B.E. CBCS Scheme Classes				
4	26-01-2017	Republic Day Celebration				
5	02-02-2017	Commencement of 4 <sup>th</sup> , Semesters of M.Tech Classes				
6	08-03-2017	Pollution testing mobile vehicle, inauguration by Sir Annamalai, Superintendent of Police Chikmagalur.				
7	11-03-2017 to 13-03-2017	Commencement of First Test Cycle for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students				
8	15-03-2017	Announcement of 1 <sup>st</sup> Test I.A Marks for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students				
	13-03-2017	Commencement of 4 <sup>th</sup> semester MBA classes				
10	17-03-2017	National Level Technical Talk by Dr.Kirankumar A S, Chairman ISRO,Bangalore				
11	25-03-2017	Workshop on Python Programming by Information Science and Engineering Department				
12	25-03-2017	Technical talk by Dr.Ananthapadmanabha, NIET,Mysore by Electrical and Electronics Engineering Department				
13	07-04-2017	Students Workshop on Design and Implementation of Communication Systems by Electronics and Communication Engineering Department				
14	07-04-2017 08-04-2017	National Conference on Emerging Trends and Advances in Information Technology NCETAIT-2017 by Computer Science and Engineering Department				
15	15-04-2017 to 17-04-2017	Commencement of Second Test Cycle for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students				
16	20-04-2017	Announcement of 2 <sup>nd</sup> Test I.A Marks for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students				
17	21-04-2017 22-04-2017	National Level Technical Paper Presentation by Electrical and Electronics Engineering Department				
18	22-04-2017 23-04-2017	National Conference by Mechanical Engineering Department				
19	27-04-2017	Workshop on Design and Development of Microcontroller and Android based Electronic System by Electronics and Communication Engineering Department				
20	15-05-2017	Graduation Day for outgoing B.E. M.Tech. and AIBM students.				
21	16-05-2017 & 17-05-2017	"CHUNCHANA"-Cultural Fest 2017				
22	22-05-2017	Last working day for 4 <sup>th</sup> semester M.Tech Students				
23	21-05-2017 to 23-05-2017	Commencement of Improvement Test Cycle for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students				
24	25-05-2017	Announcement of Improvement Test I.A Marks for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E Students				
25	02-06-2017	Last working day for 2 <sup>nd</sup> , 4 <sup>th</sup> 6 <sup>th</sup> and 8 <sup>th</sup> semester B.E and 2 <sup>nd</sup> sem M.Tech.,				

	05-06-2017 to 10-06-2017	Practical Examination for 2 <sup>nd</sup> M.Tech Students	
27	05-06-2017 to 14-06-2017	Practical Examination for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> semester B.E Students	
28	12-06-2017 to 17-06-2017	Theory Examination for 4 <sup>th</sup> semester M.Tech, students	
29	05-06-2017 to 16-06-2017	Theory Examination for 8 <sup>th</sup> semester B.E., students	
30	05-06-2017 to 17-06-2017	Theory examinations for 2 <sup>nd</sup> Semesters of M B A Students	
31	19-06-2017 to 24-06-2017	External project Viva-voce for 8 <sup>th</sup> semester students	
32	16-06-2017 to 20-07-2017	Theory examinations for 2 <sup>nd</sup> , 4 <sup>th</sup> , 6 <sup>th</sup> semester B.E Students	
33	12-06-2017 to 30-06-2017	Theory examinations for 2 <sup>nd</sup> Semesters of M-Tech. Students	
34	01-08-2017	Commencement of ODD semester tentatively	

Note : Add all the activities done by individual department and highlight the Non-Working days, Guest Lecturers, Seminars, Test Cycles etc

Copy to

1. All HOD'S
2. Placement Officer
3. Establishment Section
4. vijayalakshmi.a@spaneos.com

  
 PRINCIPAL  
 PRINCIPAL  
 Adhichunchanagiri Institute of Technology  
 CHIKMAGALUR - 577 102

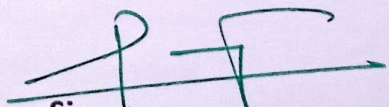


## CALENDER OF EVENTS-2021-2022

SL NO	DATE	EVENTS	REMARKS
1	08/07/2021 to 30/07/2021	Alumni webinar series	
2	08/09/2021	Submission of PG Mini project Synopsis to respective guide, PG-Coordinator and HOD	
3	13/09/2021	PG Project Work Phase-1 Final Synopsis Submission + Mini Project Proposal Seminar	
4	16/09/2021	PG Project Work Phase-1 proposal seminar	
5	20/09/21 to 01/10/2021	UG Internship Training Program	
6	20/09/2021 to 30/10/2021	PG 3 <sup>rd</sup> SEM Internship Training Program	
7	22/09/2021	PG Mini project progress seminar with board work	
8	25/09/2021	PG Mini project final draft report submission	
9	01/10/2021	Commencement of 5 <sup>TH</sup> and 7 <sup>th</sup> Semester B.E. Classes	
10	04/10/2021	Last date for submission of UG Project synopsis to guide, coordinator(s) and HOD	
11	04/10/2021	Submission of PG Project phase-1 progress report to respective guide, PG-Coordinator and HOD + internship final presentation with demo	
12	11/10/2021	One day technical talk on "Parallel computing "	
13	12/10/2021 to 13/10/2021	UG Project Proposal seminar+ PG Mini project final Demo	
14	14/10/2021	A talk by HOD on "An effective way of writing un plagiarized project reports". 7 A section	
15	15/10/2021	A talk by HOD on "An effective way of writing un plagiarized project reports". 7 B section	
16	21/10/2021	Commencement of 3 <sup>rd</sup> Semester B.E. Classes	
17	25/10/2021	Orientation class for 3rd semester 'A' section by HOD	
18	26/10/2021	Orientation class for 3rd semester 'B' section by HOD	
19	26/10/2021	Submission of PG internship synopsis to respective guide PG coordinator and HOD	
20	08/11/2021	Commencement of 3 <sup>rd</sup> Semester M.Tech Classes	
21	09/11/2021	CSI chapter inauguration	
22	19/11/2021 to 21/11/2021	Commencement of First Test Cycle for 5th and 7th Semester B.E. Students	
23	24/11/2021 to 29/11/2021	UG Project Phase-1 Progress seminar	
24	27/11/2021	AICTE Activity for 3 <sup>rd</sup> Semester students "SWACH BHARATH ABHIYANA 2021" BELAVADI	
25	30/11/2021	Announcement of 1st Test I.A Marks for 5th and 7th semester B.E Students	
26	04/12/2021	AICTE Activity for 5 <sup>TH</sup> Semester students "COVID AWARENESS and TECHNICAL TRANSFORMATION" for school students at Giriya pura	
27	08/12//2021 to 10/12/2021	Commencement of First Test Cycle for 3 <sup>RD</sup> Semester B.E. Students	
28	09/12/2021 to 10/12/2021	AICTE Activity for 7 <sup>th</sup> Semester students "Role Play and computer training for rural students"	
29	15/12/2021	Draft UG Project report submission to guide, coordinator(s) and HOD	



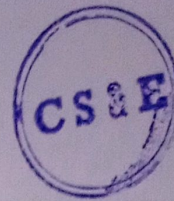
SL NO	DATE	EVENTS	REMARKS
30	17/12/2021	Announcement of 1st Test I.A Marks for 3 <sup>rd</sup> semester B.E Students	
31	22/12/2021 to 24/12/2021	Commencement of Second Test Cycle for 5th and 7th Semester B.E. Students	
32	27-12-2021 to 31/12/2021	Final UG project phase-1 Demo	
33	31/12/2021	Announcement of 2nd Test I.A Marks for 5th and 7th semester B.E Students	
34	11/01/2022 to 13/01/2022	Commencement of Second Test Cycle for 3 <sup>rd</sup> Semester B.E. Students	
35	20/01/2022	Announcement of 2nd Test I.A Marks for 3 <sup>rd</sup> semester B.E Students	
36	20/01/2022 to 22/01/2022	Commencement of 3 <sup>rd</sup> Test Cycle for 5th and 7th Semester B.E. Students	
37	26/01/2022	Announcement of 3 <sup>rd</sup> Test I.A Marks for 5th and 7th semester B.E Students	
38	14/02/2022 to 16/02/2022	Commencement of 3 <sup>rd</sup> Test Cycle for 3 <sup>rd</sup> Semester B.E. Students	
39	23/02/2022	Announcement of 3 <sup>rd</sup> Test I.A Marks for 3 <sup>rd</sup> semester B.E Students	
40	31/01/2022	Last working day for 5th and 7th semester B.E classes	
41	01/02/2022 to 10/02/2022	Practical Examinations for 5th and 7th semester B.E Students	
42	19/02/2021	Last working day for 3 <sup>rd</sup> semester B.E classes	
43	11/02/2022 to 25/03/2022	Theory Examinations for 5th and 7th semester B.E Students	
44	21/02/2021 to 04/03/2022	Practical Examinations for 3 <sup>rd</sup> semester B.E Students	
45	28/02/2022	Last working day for 3rd semester M.Tech classes	
46	01/03/2022 to 05/03/2022	Internship Viva - Voice for 3rd Semester M.Tech. Students	
47	07/03/2022 to 25/03/2022	Theory Examinations for 3 <sup>rd</sup> semester B.E Students	
48	07/03/2022 to 24/03/2022	Theory Examinations for 3rd semester M.Tech Students	
49	04/04/2022	Commencement of Even Semester for M.Tech. Students.	
50	04/04/2022	Commencement of Even Semester for B.E. students	

  
Signature of the HOD  
Professor and H.O.D.

Department of Computer Science and Engg.  
Adichunchanagiri Institute of Technology  
CHIKMAGALUR - 577102

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CALENDER OF EVENTS-2020-2021



SL NO	DATE	EVENTS	REMARKS
1	27/01/2020 to 08/02/2020	Add-On Courses on Advances in Machine Learning and Advances in Android application Development for UG	
2	31/01/2020	After availing the vacation all the faculty members are reported back to the duty	
3	08/02/2020	Project phase-II demo for PG	
4	10/02/2020 to 22/02/2020	Bridge courses for Object Oriented Programming Concepts	
5	10/02/2020	Commencement of 4rd,6th and 8th Semester B.E. Classes	
6	10/02/2020 to 15/02/2020	Proposal Technical seminar for UG(VIII Sem)	
7	18/02/2020 to 26/02/2020	UG (VIII Sem) Project seminar and Demo	
8	19/02/2020 to 27/02/2020	Add-On Courses on Artificial Intelligence for VIII Sem	
9	14/03/2020	HOD Talk for the PG students on Effective writing technical paper for International journals and conferences.	
10	02/03/2020	Final project phase-II demo and Presentation for PG	
11	02/03/2020 to 07/03/2020	Talk by HOD regarding UG project, Technical Seminar, Internship, Laboratory Evaluation and Rubrics for 4rd,6th and 8th Semester B.E. Students	
12	21/03/2020	Submission of project Phase-II draft report for PG	
13	22/03/2020 to 24/03/2020	Commencement of First Test Cycle for 4rd,6th and 8th Semester B.E. Students	
14	23/03/2020 to 28/03/2020	Final Demo and Project enhancement seminar for UG(VIII Sem)	
15	03/04/2020 to 04/04/2020	Fourth National Conference on Emerging trends and Advances in Information technology and 2 <sup>nd</sup> State Level Project Exhibition	
16	06/04/2020	Submission of plagiarized report for PG	
17	13/04/2020 to 18/04/2020	Final Technical Seminar for UG (VIII Sem)	
18	17/04/2020 to 18/04/2020	Workshop on IOT Based projects for IV Sem	
19	23/04/2020 to 25/04/2020	Commencement of Second Test Cycle for 4rd,6th and 8th Semester B.E. Students	
20	08/05/2020 to 09/05/2020	Workshop on Image Processing & it's Application for VI Sem	
21	26/05/2020 to 28/05/2020	Commencement of Third Test Cycle for 4rd,6th and 8th Semester B.E. Students	
22	28/05/2020 to 29/05/2020	Training program for faculty on Advance Teaching Learning methodology	

Signature of the HOD  
Professor and H.O.D.

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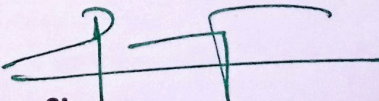
**DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING**



**CALENDER OF EVENTS-2019-2020**

SL NO	DATE	EVENTS	REMARKS
1	24/07/2019 to 27/07/2019	ICAIT 2019( First IEEE International Conference on Advances In Information Technology	
2	22/07/2019	After availing the vacation all the faculty members reported back to the duty	
3	29/07/2019	Commencement of 3rd,5th and 7th Semester B.E. Classes	
4	05/08/2019	Commencement of 1st Semester B.E. Classes	
5	05/08/2019	Commencement of Induction Program for 1st Semester BE Students	
6	07/08/2019	Submission of Internship synopsis to respective guide, PG-Coordinator and HOD	
7	07/08/2019	Orientation class for 3rd semester 'B' section	
8	08/08/2019	Orientation class for 3rd semester 'A' section	
9	10/08/2019	Last date for submission of UG Project synopsis to guide, coordinator(s) and HOD	
10	17/08/2019	PG Internship proposal seminar at 10:00 AM	
11	19/8/2019	A talk by HOD on "An effective way of writing un plagiarized project reports". 7 B section	
12	20/8/2019	A talk by HOD on "An effective way of writing un plagiarized project reports". 7 A section	
13	24/08/2019	Submission of Internship progress report to respective guide, PG-Coordinator and HOD	
14	24/08/2019	Submission of PG Project phase-1 progress report to respective guide, PG-Coordinator and HOD	
15	26/08/2019	Commencement of 3rd Semester M.Tech. Classes	
16	26/08/2019	UG Project Proposal seminar	
17	03/09/2019	PG Project phase 1 proposal seminar at 11:00AM	
18	07/09/2019	Final internship demonstration + Report submission(Draft)+ Presentation at 10:00AM for M.Tech.	
19	13/09/2019 to 15/09/2019	Commencement of First Test Cycle for 3rd,5th and 7th Semester B.E. Students	
20	14/09/2019	Submission of final PG Internship progress report to respective guide, PG-Coordinator and HOD	
21	18/09/2019	Draft UG Project report submission to guide, coordinator(s) and HOD	
22	19/09/2019	Announcement of 1st Test I.A Marks for 3rd,5th and 7th semester B.E Students	
23	21/09/2019	Submission of hard bind final PG Internship Report	
24	21/09/2019 to 23/09/2019	Commencement of First Test Cycle for 1st Semester B.E. Students	
25	27/09/2019	Announcement of 1st Test I.A Marks for 1st semester B.E Students	
26	28/09/2019	Final UG project phase 1 demo + Presentation + draft report submission	
27	30/09/2019	UG Progress seminar (final seminar for project phase - 1)	
28	5/10/2019	Submission of hard bind final UG Project phase 1 Report	
29	10/10/2019	Final report submission for UG project phase - 1	

SL NO	DATE	EVENTS	REMARKS
30	24/10/2019 to 26/10/2019	Commencement of Second Test Cycle for 3rd,5th and 7th Semester B.E. Students	
31	30/10/2019	Announcement of 2nd Test I.A Marks for 3rd,5th and 7th semester B.E Students	
32	07/11/2019 to 09/11/2019	Commencement of Second Test Cycle for 1st Semester B.E. Students	
33	14/11/2019	Announcement of 2nd Test I.A Marks for 1st semester B.E Students	
34	25/11/2019 to 27/11/2019	Commencement of Second Test Cycle for 3rd,5th and 7th Semester B.E. Students	
35	29/11/2019	Announcement of 3rd Test I.A Marks for 3rd,5th and 7th semester B.E Students	
36	30/11/2019	Last working day for 3rd, 5th and 7th semester B.E classes	
37	03/12/2020 to 13/12/2020	Practical Examinations for 3rd, 5th and 7th semester B.E Students	
38	9/12/2019 to 21/12/2019	FDP programme	
39	16/12/2020 to 18/12/2020	Commencement of Third Test Cycle for 1st Semester B.E. Students	
40	16/12/2019 to 07/02/2020	Theory Examinations for 3rd, 5th and 7th semester B.E Students	
41	20/12/2019	Announcement of 3rd Test I.A Marks for 1st semester B.E Students	
42	21/12/2019	Last working day for 1st semester M.Tech classes	
43	23/12/2019	Last working day for 3rd semester M.Tech classes	
44	23/12/2019 to 03/01/2020	Practical Examinations for 1st semester B.E Students	
45	27/12/2019 to 10/01/2020	Theory Examinations for 3rd semester M.Tech Students	
46	06/01/2020 to 28/01/2020	Theory Examinations for 1st semester B.E Students	
47	12/01/2020 to 19/01/2020	Internship Viva - Voice for 3rd Semester M.Tech. Students	
48	27/01/2020	Commencement of Even Semester for M.Tech. Students.	
49	10/2/2020	Commencement of Even Semester for B.E. students	

  
 Signature of the HOD

AIT		CLASS TIMETABLE					Format No.	ACD06		
Department		Computer Science and Engineering			Semester	'VI'	Section	'A'		
Academic Year		2021 - 2022 (EVEN Semester)			Room No.	LHD - 305				
Class Coordinator		Prof. Darshan L M								
Period	1	2	3	4	5	6	7			
Day	09:00 - 10:00	10:00 - 11:00	11:00 - 11:15	11:15 - 12:15	12:15 - 01:15	01:15 - 02:30	02:30 - 03:30	03:30 - 04:15	04:15 - 05:00	
Monday	WTA	CG	↑ B R E A K ↓	SSCD	DMDW	↑  L U N C H  ↓	← SS (A1) / CG (A2) / MAD (A3) LAB →			
Tuesday	SSC	SSCD		CG	WTA		← TUTORIALS →			
Wednesday	CG	DMDW		SSC	WTA		← SS (A2) / CG (A3) / MAD (A1) LAB →			
Thursday	SSCD	CG		DMDW	SSC		← TUTORIALS →			
Friday	← SS (A3) / CG (A1) / MAD (A2) LAB →			WTA	SSCD		DMDW			
Saturday	← CG / MAD MINI PROJECT →									

### Allocation of Subjects

#### THEORY

Subject Code	Subject Name	Faculty Name	Faculty Code
18CS61	System Software and Compilers (SSCD)	Prof. Darshan L M	
18CS62	Computer Graphics and Visualization (CG)	Prof. Anser Pasha C A	
18CS63	Web Technology and Its Application (WTA)	Prof. Bhavana D E	
18CS641	Data Mining and Data Warehousing (DMDW)	Prof. Archana P	
18EC653	Sensors and Signal Conditioning (SSC)	Prof. Divya G S	

#### PRACTICAL

Subject Code	Subject Name	Faculty Name	Faculty Code
18CSL66	System Software Lab (SS)	Prof. Darshan L M	
18CSL67	Computer Graphics Lab with Mini Project (CG)	Prof. Anser Pasha C A	
18CSMP68	Mobile Application Development (MAD)	Dr. Adarsh M J	

Signature of the Time Table Coordinators

HOD's Signature

Department of Computer Science and Engg.  
Adichunchanagiri Institute of Technology  
CHIKMAGALUR - 577102

AIT	CLASS TIME TABLE						Format No.	ACD06		
							Issue No.	01		
							Rev. No.	00		
Department	Computer Science and Engineering				Semester	'IV'	Section	'B'		
Academic Year	2021 - 2022 (EVEN Semester)				Room No.	LHD - 308				
Class Coordinator	Dr. SUNITHA M R									
Period	1	2	3	4	5	6	7			
Day	09:00 - 10:00	10:00 - 11:00	11:00 - 11:15	11:15 - 12:15	12:15 - 01:15	01:15 - 02:30	02:30 - 03:30	03:30 - 04:15	04:15 - 05:00	
Monday	OS	DAA	↑	MES	DC	↑	← TUTORIALS →			
Tuesday	DC	DAA	B	M4	OS	L	← DAA LAB / MES LAB →			
Wednesday	← DAA LAB / MES LAB →						U	OOC	DAA	CPC
Thursday	OOC	M4	E	MES	DC	N	← DAA LAB / MES LAB →			
Friday	MES	OOC	A	DAA	M4	C	OS	DC	Tutorial	
Saturday	M4	MES	K	OS	OOC	H				
			↓			↓				



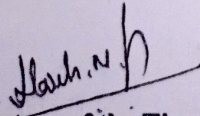
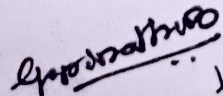
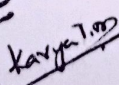
**Allocation of Subjects**

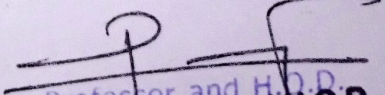
**THEORY**

Subject Code	Subject Name	Faculty Name	Faculty Code
18MAT41	Complex Analysis, Probability and Statistical Methods (M4)	Prof. Vinay K U + Prof. Chitra L S	
18CS42	Design and Analysis of Algorithms (DAA)	Prof. Arpitha C N	
18CS43	Operating Systems (OS)	Prof. Gopalakrishna C	
18CS44	Microcontroller and Embedded Systems (MES)	Prof. Chandra Naik G	
18CS45	Object Oriented Concepts (OOC)	Dr. Sunitha M R	
18CS46	Data Communication (DC)	Prof. Chethan P J	
18CPC39	Constitution of India, Professional Ethics and Cyber Law (CPC)	Prof. Divya M N	

**PRACTICAL**

Subject Code	Subject Name	Faculty Name	Faculty Code
18CSL47	Design and Analysis of Algorithm Lab	Prof. Arpitha C N + Prof. Manikanta Prasad J	
18CSL48	Microcontroller and Embedded Systems Lab	Prof. Chandra Naik G + Prof. Gopalakrishna C	

  
  
  
 Signature of the Time Table Coordinators

  
 Professor and H.O.D.  
 Department of Computer Science and Engg.  
 Adichunchanagiri Institute of Technology  
 Adichunchanagiri Institute of Technology  
 CHIKMAGALUR - 577 102

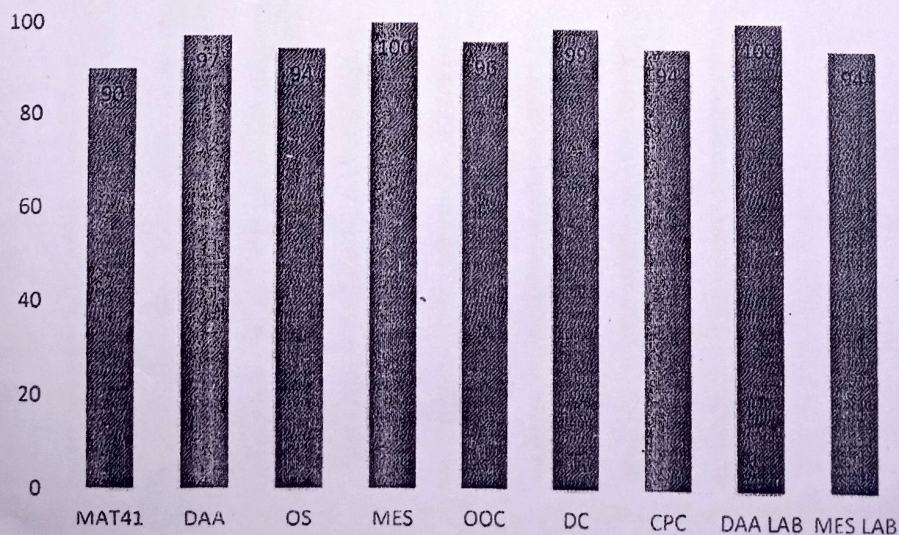


ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY, CHIKMAGALUR  
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING  
SUBJECT WISE RESULT ANALYSIS OF 2021-22

SEMESTER : 4TH B

Sl.No	Subject Name	Student Strength	Appear ed	PASS	Percent age	Faculty Name
1	Complex Analysis, Probability and Statistical Methods(18MAT41)	69	69	61	90	Vinay+Chitra L S
2	Design and Analysis of Algorithms(18CS42)	69	69	67	97	Arpitha C N
3	Operating System(18CS43)	69	69	65	94	Gopalkrishna C
4	Micro Controller and Embeded System(18CS44)	69	69	69	100	Chandra Naik G
5	Object oriented Concepts(18CS45)	69	69	66	96	Dr.Sunitha M R
6	Data Communications(18CS46)	69	69	68	99	Chethan P J
7	Constitution of India , Profession Ethics and Cyber Law(18CPC49)	69	69	65	94	Divya M N
8	Design and Analysis of Algorithms Lab(18CSL47)	69	69	69	100	Arpitha C N
9	Micro Controller and Embeded System Lab(18CSL48)	69	69	65	94	Chandra Naik G

Total No.of Students Appeared- 69  
Total No.oof Pass- 57  
Total No.of Fail- 12  
% of Students Passed 83



Professor and H.O.D.

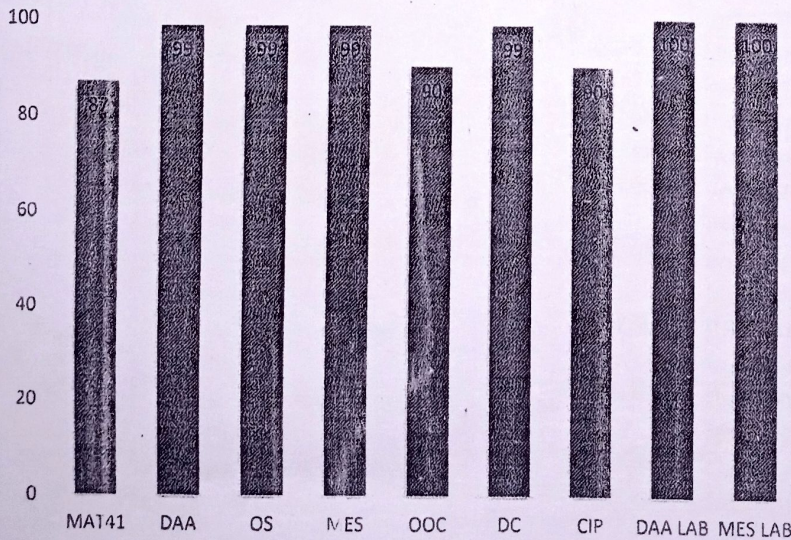
**ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY, CHIKMAGALUR**  
**DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING**  
**SUBJECT WISE RESULT ANALYSIS OF 2021-22**



**SEMESTER : 4 A**

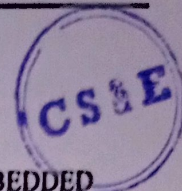
Sl. No	Subject Name	Student Strength	Appear ed	PASS	Percent age	Faculty Name
1	Complex Analysis, Probability and Statstical Methods(18MAT41)	69	69	60	87	Mallikarjun H B + Rashmi
2	Design and Analysis of Algorithms(18CS42)	69	69	68	99	S J Prashantha
3	Operating System(18CS43)	69	69	68	99	Harish S
4	Micro Controller and Embeded System(18CS44)	69	69	68	99	Dr. Madhu B G
5	Object oriented Concepts(18CS45)	69	69	62	90	Sathyabhama R
6	Data Communications(18CS46)	69	69	68	99	Gopinath C B
7	Constitution of India , Profession Ethics and Cyber Law(18CPC49)	69	69	62	90	Divya M N
8	Design and Analysis of Algorithms Lab(18CSL47)	69	69	69	100	S J Prashantha
9	Micro Controller and Embeded System Lab(18CSL48)	69	69	69	100	Dr. Madhu B G

**Total No.of Students Appeared- 69**  
**Total No.oof Pass- 53**  
**Total No.of Fail- 16**  
**% of Students Passed 77**



**Professor and H.O.D.**  
 Department of Computer Science and Engg.  
 Adichunchanagiri Institute of Technology  
 CHIKMAGALUR - 577102





### 6. Course Information

6.2

Semester : 4

Section : A

Course : MICROCONTROLLER AND EMBEDDED SYSTEMS

P e r i o d	Planned			Execution		
	Date	Topic	Source material to be referred	Date	Topic	Source material to be referred
<b>1</b>						
1	2022-05-23	Microprocessors versus Microcontrollers, ARM Embedded Systems- The RISC design philosophy	Text 1	2022-05-23	Microprocessors versus Microcontrollers, ARM Embedded Systems- The RISC design philosophy	Text 1
2	2022-05-24	The ARM Design Philosophy, Embedded System Hardware		2022-05-24	The ARM Design Philosophy, Embedded System Hardware	Text 1
3	2022-05-25	Embedded System Software		2022-05-25	Embedded System Software	Text 1
4	2022-05-26	ARM Processor Fundamentals- Registers		2022-05-26	ARM Processor Fundamentals- Registers	Text 1
5	2022-05-30	Current Program Status Register		2022-05-30	Current Program Status Register	Text 1
6	2022-05-31	Pipeline		2022-05-31	Pipeline	Text 1
7	2022-06-01	Exceptions		2022-06-01	Exceptions	Text 1
8	2022-06-02	Interrupts		2022-06-02	Interrupts	Text 1
9	2022-06-06	and the Vector Table		2022-06-06	and the Vector Table	Text 1
10	2022-06-07	Core Extensions		2022-06-07	Core Extensions	Text 1
<b>2</b>						
11	2022-06-08	Data Processing Instructions		2022-06-08	Data Processing Instructions	Text 1
12	2022-06-09	Data Processing Instructions		2022-06-09	Data Processing Instructions	Text 1
13	2022-06-13	Programme Instructions		2022-06-13	Programme Instructions	Text 1
14	2022-06-14	Programme Instructions		2022-06-14	Programme Instructions	Text 1
15	2022-06-15	Software Interrupt Instructions		2022-06-15	Software Interrupt Instructions	Text 1
16	2022-06-16	Software Interrupt Instructions		2022-06-16	Software Interrupt Instructions	Text 1
17	2022-06-20	Program Status Register Instructions		2022-06-20	Program Status Register Instructions	Text 1
18	2022-06-21	Program Status Register Instructions		2022-06-21	Program Status Register Instructions	Text 1
19	2022-06-22	Coprocessor Instructions		2022-06-22	Coprocessor Instructions	Text 1
20	2022-06-23	Loading Constants		2022-06-23	Loading Constants	Text 1



3						
21	2022-06-27	Embedded Vs General computing system, History of embedded systems		2022-06-27	Embedded Vs General computing system, History of embedded systems	Text 2
22	2022-06-28	Classification of Embedded systems, Major applications areas of embedded systems		2022-06-28	Classification of Embedded systems, Major applications areas of embedded systems	Text 2
23	2022-06-29	purpose of embedded systems Core of an Embedded System including all types of processor/controller, Memory		2022-06-29	purpose of embedded systems Core of an Embedded System including all types of processor/controller, Memory	Text 2
24	2022-06-30	Sensors, Actuators		2022-06-30	Sensors, Actuators	Text 2
25	2022-07-04	LED, 7 segment LED display		2022-07-04	LED, 7 segment LED display	Text 2
26	2022-07-05	stepper motor, Keyboard		2022-07-05	stepper motor, Keyboard	Text 2
27	2022-07-06	Push button switch		2022-07-06	Push button switch	Text 2
28	2022-07-07	Communication Interface (onboard and external types)		2022-07-07	Communication Interface (onboard and external types)	Text 2
29	2022-07-11	Embedded firmware		2022-07-11	Embedded firmware	Text 2
30	2022-07-12	Other system components		2022-07-12	Other system components	Text 2

4						
31	2022-07-13	Characteristics and Quality Attributes of Embedded Systems		2022-07-13	Characteristics and Quality Attributes of Embedded Systems	Text 2
32	2022-07-14	Characteristics and Quality Attributes of Embedded Systems		2022-07-14	Characteristics and Quality Attributes of Embedded Systems	Text 2
33	2022-07-18	Operational quality attributes		2022-07-18	Operational quality attributes	Text 2
34	2022-07-19	Operational quality attributes		2022-07-19	Operational quality attributes	Text 2
35	2022-07-20	non-operational quality attributes		2022-07-20	non-operational quality attributes	Text 2
36	2022-07-21	non-operational quality attributes		2022-07-21	non-operational quality attributes	Text 2
37	2022-07-25	Embedded Systems- Application and Domain specific		2022-07-25	Embedded Systems- Application and Domain specific	Text 2
38	2022-07-26	Embedded Systems- Application and Domain specific		2022-07-26	Embedded Systems- Application and Domain specific	Text 2
39	2022-07-27	Hardware Software Co-Design and Program Modelling		2022-07-27	Hardware Software Co-Design and Program Modelling	Text 2
40	2022-07-28	embedded firmware design and development		2022-07-28	embedded firmware design and development	Text 2



3						
41	2022-08-01	Operating System basics, Types of operating systems		2022-08-01	Operating System basics, Types of operating systems	Text 2
42	2022-08-02	Task, process and threads (Only POSIX Threads with an example program)		2022-08-02	Task, process and threads (Only POSIX Threads with an example program)	Text 2
43	2022-08-03	Thread preemption, Multiprocessing and Multitasking		2022-08-03	Thread preemption, Multiprocessing and Multitasking	Text 1
44	2022-08-04	Task Communication (without any program), Task synchronization issues – Racing and Deadlock		2022-08-04	Task Communication (without any program), Task synchronization issues – Racing and Deadlock	Text 1
45	2022-08-08	Concept of Binary and counting semaphores (Mutex example without any program), How to choose an RTOS		2022-08-08	Concept of Binary and counting semaphores (Mutex example without any program), How to choose an RTOS	Text 2
46	2022-08-10	Integration and testing of Embedded hardware and firmware, Embedded system Development Environment – Block diagram (excluding Keil)		2022-08-10	Integration and testing of Embedded hardware and firmware, Embedded system Development Environment – Block diagram (excluding Keil)	Text 1
47	2022-08-11	Disassembler/decompiler, simulator		2022-08-11	Disassembler/decompiler, simulator	Text 1
48	2022-08-16	emulator and debugging techniques		2022-08-16	emulator and debugging techniques	Text 1
49	2022-08-17	target hardware debugging		2022-08-17	target hardware debugging	Text 1
50	2022-08-18	boundary scan		2022-08-18	boundary scan	Text 1



6. Course Information

6.4 Internal Assessment

Internal : 1

Semester:4-CBCS 2018

Date : 02/07/2022

Subject : MICROCONTROLLER AND EMBEDDED SYSTEMS (18CS44)

Time : 12:00 - 13:00

Faculty : Madhu B G

Max Marks: 50

Answer Any 5 Questions				
Q.No		Max Marks	CO	BT/CL
1	<i>Illustrate How ARM is better for embedded systems than Intel x86? Discuss its Architectural Design rules and Philosophy.</i>	10	1	L1
2	<i>Along with neat diagram of an ARM based embedded device (Microcontroller), explain the four main hardware components.</i>	10	2	L2



3	Explain ARM core data flow model with a neat diagram.	10	2	L2
4	Give the schematic of a Current Program Status Register of ARM7 processor, explain the individual bits and illustrate the different processor modes provided by ARM7.	10	2	L2
5	What is pipeline in ARM? Illustrate with an example. Show the pipeline stages of ARM7, ARM9 and ARM10	10	2	L2



6	<p><i>Explain the following data processing instructions with examples.</i></p> <p><i>a. Arithmetic Instructions</i></p> <p><i>b. Multiplication Instructions.</i></p>	10	2	L2
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### Evaluation

USN	Name	Present (P) / Absent (Ab)	Q1	Q2	Q3	Q4	Q5	Q6	IA Total	BT/CL
4AI19CS057	Koushik Lokesh K	P	8	9	8	7	9	0	41	Understand
4AI20CS003	Abhijna.CD	P	0	10	10	6	9	6	41	Understand
4AI20CS005	Aishwaraya A S	P	0	10	8	2	4	2	26	Understand
4AI20CS006	Aisiri H T	P	10	0	10	7	8	7	42	Understand
4AI20CS007	Amith Shetty A R	P	0	7	7	0	2	0	16	Understand
4AI20CS008	Amulya J	P	0	8	10	7	8	9	42	Understand
4AI20CS010	Anil Kumar R. Patil	P	0	10	10	0	7	0	27	Understand
4AI20CS011	Anjanni S Singh	P	6	10	10	6	6	0	38	Understand
4AI20CS012	Anvika B N	P	0	10	10	8	7	4	39	Understand
4AI20CS014	Arya K S	P	7	8	10	9	7	0	41	Understand
4AI20CS016	Bhakti Sampada J S	P	0	10	10	6	4	0	30	Understand
4AI20CS018	Bhoomika G V	P	10	0	0	10	8	8	36	Understand
4AI20CS022	Chandrashekhara M	P	0	9	8	8	7	0	32	Understand
4AI20CS023	Chethan K S	P	8	0	9	9	10	10	46	Understand
4AI20CS026	Daivik M D	P	8	10	9	0	2	4	33	Understand
4AI20CS027	Deeksha C T	P	0	0	10	8	0	0	18	Understand
4AI20CS028	Deekshith K	P	1	7	7	6	4	0	25	Understand
4AI20CS034	Gagana K R	P	8	0	10	10	8	10	46	Understand
4AI20CS035	Harshith M O	P	0	8	7	4	4	0	23	Understand
4AI20CS037	Harshitha K	Ab	0	0	0	0	0	0	0	No Level
4AI20CS038	Himasiri U A	P	0	7	8	0	7	2	24	Understand
4AI20CS040	Inchara B E	P	4	10	10	7	7	0	38	Understand



USN	Name	Present (P) / Absent (Ab)	Q1	Q2	Q3	Q4	Q5	Q6	IA Total	BT/CL
4AI20CS041	Jeevan D S	P	5	9	9	8	8	0	39	Understand
4AI20CS047	Kishan S Doddamani	P	2	9	0	10	7	9	37	Understand
4AI20CS048	Kubra Fathima	P	0	10	10	10	10	10	50	Understand
4AI20CS050	Likith M D	P	0	10	9	5	6	4	34	Understand
4AI20CS053	Manjunatha B	P	8	9	9	10	10	0	46	Understand
4AI20CS064	Nisarga S N	P	2	10	7	8	6	0	33	Understand
4AI20CS066	Pannaga B M	P	9	10	10	9	0	7	45	Understand
4AI20CS067	Pavan N	P	9	9	7	10	8	0	43	Understand
4AI20CS070	Preetham Gowda C B	P	0	9	8	6	0	2	25	Understand
4AI20CS071	Priya K M	P	0	10	10	9	10	8	47	Understand
4AI20CS072	Priyanka K	P	0	10	10	8	10	8	46	Understand
4AI20CS073	Rakshita	P	0	8	5	9	0	0	22	Understand
4AI20CS076	Ram Kumar G	P	6	10	10	8	0	6	40	Understand
4AI20CS078	Ratan Akki	P	10	10	10	10	9	0	49	Understand
4AI20CS080	Roopesh B Y	P	3	8	8	8	10	0	37	Understand
4AI20CS081	Saba Fathima	P	8	10	10	8	8	0	44	Understand
4AI20CS082	Sahana	P	0	10	0	10	10	7	37	Understand
4AI20CS084	Sahana R	P	7	10	8	10	0	0	35	Understand
4AI20CS086	Samanvaya K J	P	4	10	0	10	10	0	34	Understand
4AI20CS087	Sanath D U	P	0	0	10	6	0	0	16	Understand
4AI20CS089	Sanjana S	P	6	8	9	8	9	0	40	Understand
4AI20CS091	Sapnaz Banu I	P	6	10	10	5	5	0	36	Understand
4AI20CS093	Savitra Shenoy	P	9	10	10	10	10	0	49	Understand
4AI20CS095	Shishira B J	P	8	9	10	10	7	0	44	Understand
4AI20CS097	Shreehari S Rao	P	3	6	4	6	6	0	25	Understand
4AI20CS101	Sinchana B R	P	0	2	10	10	8	9	39	Understand
4AI20CS103	Sridevi K S	P	0	7	10	10	8	9	44	Understand
4AI20CS105	Sudhanva A P	P	0	6	10	9	7	0	32	Understand
4AI20CS107	Suhas	P	0	10	10	10	10	6	46	Understand
4AI20CS109	Sujay G	P	0	8	1	8	5	4	26	Understand
4AI20CS111	Sumedh M Anvekar	P	4	10	10	7	10	0	41	Understand
4AI20CS114	Tanushree D	P	0	9	5	2	5	0	21	Understand
4AI20CS115	Uday Rao C G	P	0	6	9	5	4	2	26	Understand
4AI20CS116	Vamshika M J	P	6	10	9	10	4	0	39	Understand
4AI20CS117	Varsha B C	P	0	10	10	9	7	0	36	Understand
4AI20CS118	Varsha V	P	4	10	5	5	10	0	34	Understand
4AI20CS119	Varshini G	P	2	10	8	10	5	0	35	Understand



USN	Name	Present (P) / Absent (Ab)	Q1	Q2	Q3	Q4	Q5	Q6	IA Total	BT/CL
4AI20CS120	Vidya N G	P	0	8	6	0	2	0	16	Understand
4AI20CS121	Vijayalakshmi G S	P	0	10	10	9	10	2	41	Understand
4AI20CS124	Vivek B R	P	0	8	6	6	4	0	24	Understand
4AI20CS125	Yashaswini R	P	0	10	10	8	10	0	38	Understand
4AI20CS127	Bhoomika S P	P	0	10	10	0	6	0	26	Understand
4AI21CS401	Manoj Kumar U	P	0	10	10	0	8	0	28	Understand
4AI21CS403	Mohan D S	P	0	9	10	9	7	1	36	Understand
4AI21CS405	Ranjita Laxman Naik	P	6	9	9	0	6	2	32	Understand
4AI21CS407	Sanjay K Y	P	10	0	8	4	0	6	28	Understand
4AI21CS410	Vinay M N	P	4	10	10	8	9	0	41	Understand



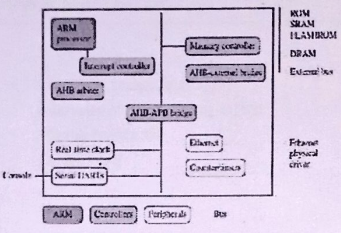
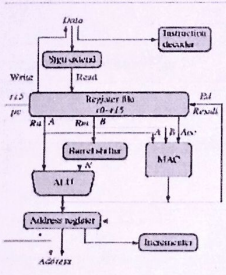


2 Scheme of Evaluation

SCHEME AND SOLUTIONS

Course Title: MicroController and Embedded System

Course Code: 18CS44

Q. No.	SOLUTION	MARKS ALLOCATED
1.	Differences Between CISC and RISC RISC Design Rules Instructions Pipelines Registers Load-store architecture	05Marks
2.	Diagram:  <p>Figure 1.2 An example of an ARM based embedded device, a microcontroller.</p> <p>Explanation: The device into four main hardware components:</p> <ul style="list-style-type: none"> <li>- ARM processor</li> <li>- Controllers</li> <li>- Peripherals</li> <li>- Bus</li> </ul>	05Marks
3.	Diagram:  <p>Figure 2.1 ARM core datapath model.</p> <p>Explanation each feature :</p>	05 Marks



Course Title: MicroController and Embedded System

Course Code: 18CS44

Q. No.	SOLUTION	MARKS ALLOCATED
4.	<p><b>Diagram:</b></p> <p>Figure 2.3 A generic program status register (<i>psr</i>).</p> <p><b>Explanation</b></p> <ul style="list-style-type: none"> <li>• Condition Codes: N, Z, C, V</li> <li>• Interruption mask: I (IRQ), F (FIQ)</li> <li>• Thumb Enable Bit</li> <li>• Mode (5-bit)</li> </ul>	05 Marks
5.	<p><b>Definition :</b></p> <p><b>Diagram:</b></p> <p>Figure 2.7 ARM7 Three-stage pipeline.</p> <p>Figure 2.8 Pipelined instruction sequence.</p> <p>Figure 2.9 ARM9 five-stage pipeline.</p> <p>Figure 2.10 ARM10 six-stage pipeline.</p> <p><b>Explanation :</b></p>	01 Mark 05 Mark  04 Mark



Course Title: Micro Controller and Embedded System

Course Code: 18CS44

Q. No.	SOLUTION	MARKS ALLOCATED																		
	<p><b>Arithmetic Instructions</b></p> <p>Syntax: &lt;instruction&gt;{&lt;cond&gt;[!S]} Rd, Rn, N</p> <table border="1"> <tr> <td>ADC</td> <td>add two 32-bit values and carry</td> <td><math>Rd = Rn + N + \text{carry}</math></td> </tr> <tr> <td>ADD</td> <td>add two 32-bit values</td> <td><math>Rd = Rn + N</math></td> </tr> <tr> <td>RSB</td> <td>reverse subtract of two 32-bit values</td> <td><math>Rd = N - Rn</math></td> </tr> <tr> <td>RSC</td> <td>reverse subtract with carry of two 32-bit values</td> <td><math>Rd = N - Rn - 1(\text{carry flag})</math></td> </tr> <tr> <td>SBC</td> <td>subtract with carry of two 32-bit values</td> <td><math>Rd = Rn - N - 1(\text{carry flag})</math></td> </tr> <tr> <td>SUB</td> <td>subtract two 32-bit values</td> <td><math>Rd = Rn - N</math></td> </tr> </table> <p>N is the result of the shifter operation. The syntax of shifter operation is shown in Table 3.3.</p>	ADC	add two 32-bit values and carry	$Rd = Rn + N + \text{carry}$	ADD	add two 32-bit values	$Rd = Rn + N$	RSB	reverse subtract of two 32-bit values	$Rd = N - Rn$	RSC	reverse subtract with carry of two 32-bit values	$Rd = N - Rn - 1(\text{carry flag})$	SBC	subtract with carry of two 32-bit values	$Rd = Rn - N - 1(\text{carry flag})$	SUB	subtract two 32-bit values	$Rd = Rn - N$	05 Marks
ADC	add two 32-bit values and carry	$Rd = Rn + N + \text{carry}$																		
ADD	add two 32-bit values	$Rd = Rn + N$																		
RSB	reverse subtract of two 32-bit values	$Rd = N - Rn$																		
RSC	reverse subtract with carry of two 32-bit values	$Rd = N - Rn - 1(\text{carry flag})$																		
SBC	subtract with carry of two 32-bit values	$Rd = Rn - N - 1(\text{carry flag})$																		
SUB	subtract two 32-bit values	$Rd = Rn - N$																		
6.	<p><b>Multiply Instructions</b></p> <p>Syntax: MUA{&lt;cond&gt;}[S] Rd, Rn, Rs, Rr MUE{&lt;cond&gt;}[S] Rd, Rn, Rs</p> <table border="1"> <tr> <td>HA</td> <td>multiply and accumulate</td> <td><math>Rd = (Rn * Rs) + Rr</math></td> </tr> <tr> <td>MUL</td> <td>multiply</td> <td><math>Rd = Rn * Rs</math></td> </tr> </table> <p>Syntax: &lt;instruction&gt;{&lt;cond&gt;[!S]} rdLo, RSHi, Rn, Rs</p> <table border="1"> <tr> <td>SMUL</td> <td>signed multiply accumulate long</td> <td><math>[RdHi, RdLo] = [RnHi, RnLo] + (Rr * Rs)</math></td> </tr> <tr> <td>SMULL</td> <td>signed multiply long</td> <td><math>[RdHi, RdLo] = Rn * Rs</math></td> </tr> <tr> <td>UMUL</td> <td>unsigned multiply accumulate long</td> <td><math>[RdHi, RdLo] = [RnHi, RnLo] + (Rr * Rs)</math></td> </tr> <tr> <td>UMULL</td> <td>unsigned multiply long</td> <td><math>[RdHi, RdLo] = Rn * Rs</math></td> </tr> </table>	HA	multiply and accumulate	$Rd = (Rn * Rs) + Rr$	MUL	multiply	$Rd = Rn * Rs$	SMUL	signed multiply accumulate long	$[RdHi, RdLo] = [RnHi, RnLo] + (Rr * Rs)$	SMULL	signed multiply long	$[RdHi, RdLo] = Rn * Rs$	UMUL	unsigned multiply accumulate long	$[RdHi, RdLo] = [RnHi, RnLo] + (Rr * Rs)$	UMULL	unsigned multiply long	$[RdHi, RdLo] = Rn * Rs$	05 Marks
HA	multiply and accumulate	$Rd = (Rn * Rs) + Rr$																		
MUL	multiply	$Rd = Rn * Rs$																		
SMUL	signed multiply accumulate long	$[RdHi, RdLo] = [RnHi, RnLo] + (Rr * Rs)$																		
SMULL	signed multiply long	$[RdHi, RdLo] = Rn * Rs$																		
UMUL	unsigned multiply accumulate long	$[RdHi, RdLo] = [RnHi, RnLo] + (Rr * Rs)$																		
UMULL	unsigned multiply long	$[RdHi, RdLo] = Rn * Rs$																		

**Internal : 2**

Semester:4-CBCS 2018

Subject : MICROCONTROLLER AND EMBEDDED SYSTEMS (18CS44)

Faculty : Madhu B G

Date : 01/08/2022

Time : 12:00 - 13:00

Max Marks: 50

**Answer Any 5 Questions**



Q.No		Max Marks	CO	BT/CL
1	<i>Explain the ARM Single-Register and Multiple-Register load-store addressing modes with example.</i>	10	2	L2
2	<i>Explain the ARM swap instruction and branch instruction with an example code.</i>	10	2	L2
3	<i>Explain the system core of the Embedded systems.</i>	10	2	L2



4	<p>What are the different types of memories used in Embedded System design? Explain the role of each:</p>	10	2	L2
5	<p>Explain the interfacing of following I/O subsystems with Embedded systems i) 7-Segment LED Display ii) Stepper Motor.</p>	10	3	L2
6	<p>Explain the sequence of operation for communicating with an I2C slave device.</p>	10	3	L2



7	<p>Explain the following: i. 1-Wire Interface ii. SPI Interface iii. Reset Circuit iv. Parallel Interface</p>	10	2	L2
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### Evaluation

USN	Name	Present (P) / Absent (Ab)	Q1	Q2	Q3	Q4	Q5	Q6	Q7	IA Total	BT/CL
4AI19CS057	Koushik Lokesh K	P	0	9	10	10	10	10	0	49	Understand
4AI20CS003	Abhijna C D	P	10	10	0	10	9	10	0	49	Understand
4AI20CS005	Aishwaraya A S	P	4	5	1	1	10	0	0	21	Understand
4AI20CS006	Aisiri H T	P	10	0	0	10	10	10	8	48	Understand
4AI20CS007	Amith Shetty A R	P	0	0	0	8	7	0	0	15	Understand
4AI20CS008	Amulya J	P	10	10	0	10	8	10	0	48	Understand
4AI20CS010	Anil Kumar R Patil	P	0	0	7	10	10	0	0	27	Understand
4AI20CS011	Anjanni S Singh	P	0	0	9	10	10	10	3	42	Understand
4AI20CS012	Anvika B N	P	10	8	9	10	0	10	0	47	Understand
4AI20CS014	Arya K S	P	0	8	10	10	10	10	0	48	Understand
4AI20CS016	Bhakti Sampada J S	P	0	4	0	10	6	9	0	29	Understand
4AI20CS018	Bhoomika G V	P	0	5	2	10	9	10	0	36	Understand
4AI20CS022	Chandrashekhara M	P	1	10	8	10	10	10	0	48	Understand
4AI20CS023	Chethan K S	P	10	0	0	8	10	10	8	46	Understand
4AI20CS026	Daivik M D	P	0	0	0	7	5	6	0	18	Understand
4AI20CS027	Deeksha C T	P	10	0	10	5	4	10	0	39	Understand
4AI20CS028	Deekshith K	P	0	0	0	8	9	7	7	31	Understand
4AI20CS034	Gagana K R	P	10	9	0	10	10	10	0	49	Understand
4AI20CS035	Harshith M O	P	0	0	0	0	5	6	0	11	Understand
4AI20CS037	Harshitha K	P	0	0	6	7	7	5	5	30	Understand
4AI20CS038	Himasiri U A	P	3	0	3	2	4	0	0	12	No Level
4AI20CS040	Inchara B E	P	4	0	0	7	7	8	0	26	Understand



# ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY

## Department of Computer Science and Engineering (CS)

USN	Name	Present (P) / Absent (Ab)	Q1	Q2	Q3	Q4	Q5	Q6	Q7	IA Total	BT/CL
4AI20CS041	Jeevan D S	P	2	0	0	6	8	9	0	25	Understand
4AI20CS047	Kishan S Doddamani	P	6	3	0	0	0	8	6	23	Understand
4AI20CS048	Kubra Fathima	P	10	10	8	10	10	0	0	48	Understand
4AI20CS050	Laksh M D	P	0	0	7	8	6	5	0	26	Understand
4AI20CS053	Manjunatha B	P	0	5	10	10	10	7	6	43	Understand
4AI20CS064	Nisarga S N	P	0	0	9	10	10	7	0	36	Understand
4AI20CS066	Pannaga B M	P	10	10	0	10	10	5	0	45	Understand
4AI20CS067	Pavan N	P	9	0	10	7	10	2	0	38	Understand
4AI20CS070	Preetham Gowda C B	P	8	8	0	0	10	10	0	36	Understand
4AI20CS071	Priya K M	P	10	10	9	10	10	0	0	49	Understand
4AI20CS072	Priyanka K	P	8	9	0	10	10	0	8	45	Understand
4AI20CS073	Rakshita	P	10	0	1	10	10	0	0	31	Understand
4AI20CS076	Ram Kumar G	P	7	0	0	8	9	6	1	31	Understand
4AI20CS078	Ratan Akki	P	8	0	10	10	10	10	0	48	Understand
4AI20CS080	Roopesh B Y	P	0	5	5	10	8	7	0	35	Understand
4AI20CS081	Saba Fathima	P	0	5	7	6	10	0	0	28	Understand
4AI20CS082	Sahana	P	10	8	5	8	10	3	3	41	Understand
4AI20CS084	Sahana R	P	10	10	0	10	10	9	8	49	Understand
4AI20CS086	Samanvaya K J	P	0	0	10	10	10	10	10	50	Understand
4AI20CS087	Sanath D U	P	0	0	4	7	10	3	0	24	Understand
4AI20CS089	Sanjana S	P	0	5	6	10	10	10	0	41	Understand
4AI20CS091	Sapnaz Banu I	P	10	5	8	0	10	10	0	43	Understand
4AI20CS093	Savitra Shenoy	P	0	10	10	8	10	10	0	48	Understand
4AI20CS095	Shishira B J	P	4	4	8	10	0	10	10	42	Understand
4AI20CS097	Shrechari S Rao	P	0	0	0	10	8	10	0	28	Understand
4AI20CS101	Sinchana B R	P	6	0	10	10	10	10	0	46	Understand
4AI20CS103	Sridevi K S	P	0	9	10	10	10	10	0	49	Understand
4AI20CS105	Sudhanva A P	P	0	2	8	8	6	9	0	33	Understand
4AI20CS107	Suhas	P	10	0	0	10	9	10	8	47	Understand
4AI20CS109	Sujay G	P	0	5	8	6	7	0	0	26	Understand
4AI20CS111	Sumodh M Anvekar	P	10	5	0	10	10	10	10	50	Understand
4AI20CS114	Tanushree D	P	0	0	0	10	10	8	8	36	Understand
4AI20CS115	Uday Rao C G	P	0	1	10	7	10	1	0	29	Understand
4AI20CS116	Vamshika M J	P	5	0	7	10	10	9	0	41	Understand
4AI20CS117	Varsha B C	P	9	0	0	10	10	10	8	47	Understand
4AI20CS118	Varsha V	P	0	0	10	10	9	10	0	39	Understand
4AI20CS119	Varshini G	P	10	0	0	10	10	8	8	46	Understand



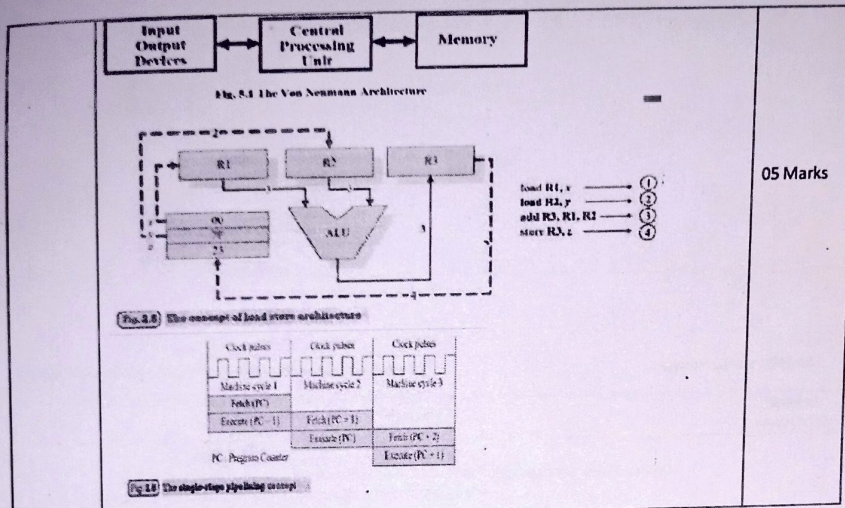
# ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY

## Department of Computer Science and Engineering (CS)

USN	Name	Present (P) / Absent (Ab)	Q1	Q2	Q3	Q4	Q5	Q6	Q7	IA Total	BT/CL
4AI20CS120	Vidya N G	P	0	0	9	0	6	9	8	32	Understand
4AI20CS121	Vijayalakshmi G S	P	0	0	8	10	10	8	0	36	Understand
4AI20CS124	Vivek B R	P	1	0	6	8	10	3	0	28	Understand
4AI20CS125	Yashaswini R	P	0	0	8	10	5	10	8	41	Understand
4AI20CS127	Bhoomika S P	P	0	0	10	3	0	10	10	33	Understand
4AI21CS401	Manoj Kumar U	P	0	0	10	10	3	7	0	30	Understand
4AI21CS403	Mohan D S	P	0	9	10	10	10	8	0	47	Understand
4AI21CS405	Ranjita Laxman Naik	P	4	0	7	10	7	2	0	30	Understand
4AI21CS407	Sanjay K Y	P	0	5	5	10	10	3	0	33	Understand
4AI21CS410	Vinay M N	P	7	4	10	8	10	0	0	39	Understand





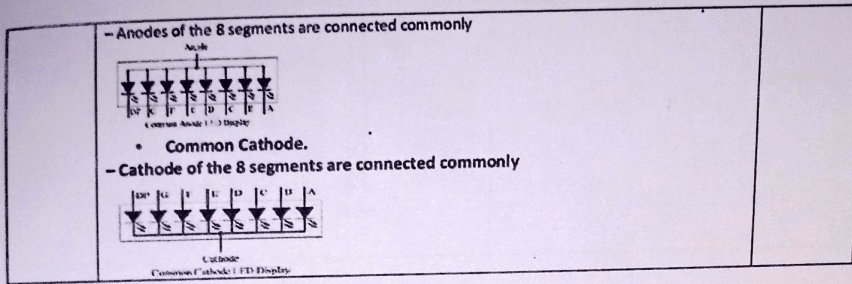


05 Marks

Course Title: MicroController and Embedded System

Course Code: 18CS44

Q. No.	SOLUTION	MARKS ALLOCATED
4.	<p>1. Types of Memory in Embedded System:</p> <ul style="list-style-type: none"> <li>A) Program Storage Memory (ROM)</li> <li>b) Masked ROM (MROM)</li> <li>c) Programmable Read Only Memory (PROM)/ (OTP)</li> <li>d) Erasable Programmable Read Only Memory (EPROM)</li> <li>e) Electrically Erasable Programmable Read Only Memory (EEPROM)</li> <li>f) FLASH</li> </ul> <p>2. Read-Write Memory/Random Access Memory (RAM)</p> <ul style="list-style-type: none"> <li>a) Static RAM (SRAM)</li> <li>b) Dynamic RAM (DRAM)</li> <li>c) Non-volatile RAM(NVRAM)</li> </ul>	<p>05 Marks</p> <p>05 Marks</p>
5.	<p>7-segment LED display:</p> <ul style="list-style-type: none"> <li>• Is an output device for displaying alphanumeric characters.</li> <li>• It contains 8 LED segments arranged in a special form.</li> <li>• Out of the 8 LED segments               <ul style="list-style-type: none"> <li>- 7 are used for displaying alphanumeric characters and are named as A-G</li> <li>- 1 is used for representing 'decimal point' in decimal number display.</li> </ul> </li> </ul> <div style="text-align: center;"> </div> <p>• Common Anode</p>	05 Mark



Course Title: Micro Controller and Embedded System

Course Code: 18CS44

Q. No.	SOLUTION	MARKS ALLOCATED
	<p>A stepper motor:</p> <ul style="list-style-type: none"> <li>• Is an electro-mechanical device which generates discrete displacement (motion) in response to dc electrical signals.</li> <li>• It differs from the normal dc motor in its operation.               <ul style="list-style-type: none"> <li>- DC motor produces continuous rotation on applying dc voltage</li> <li>- Stepper motor produces discrete rotation in response to the dc voltage applied to it.</li> </ul> </li> <li>• The paper feed mechanism of a printer/fax makes use of stepper motors for its functioning.</li> </ul> <p>Fig. 8.10 2-Phase bipolar stepper motor</p>	05 Marks
6	<p>Inter Integrated Circuit (I2C) Bus:</p> <p>2 Buses: SCL, SDA</p> <p>2 Devices : Master &amp; Slave</p> <p>Master Device: For controlling</p> <ul style="list-style-type: none"> <li>• Initiating/terminating data transfer</li> <li>• Sending data</li> <li>• Generating necessary synchronization clock pulses.</li> </ul> <p>Slave Device:</p> <ul style="list-style-type: none"> <li>• Wait for the commands from the master</li> <li>• Respond upon receiving the commands.</li> </ul>	05 Marks

	<p>Fig. 3.30 I2C Bus Interfacing</p>	05 Marks
7	<p>Working of 1-wire interface:</p> <ol style="list-style-type: none"> <li>1. Master device :- sends a 'Reset' pulse on 1-wire bus.</li> <li>2. Slave device connected to that wire responds as 'Present' pulse</li> <li>3. Master send a ROM command: initiates communication at slave device.</li> <li>4. Master send read/ write of control command</li> <li>5. Master device initiates read / write data between devices</li> </ol> <p>Fig. 3.31 I2C Bus Interfacing</p> <p>Fig. 3.32 Bus Interfacing</p>	05 Marks

Internal : 3

Semester:4-CBCS 2018

Subject : MICROCONTROLLER AND EMBEDDED SYSTEMS (18CS44)

Faculty : Madhu B G

Date : 28/08/2022

Time : 12:00 - 13:00

Max Marks: 50

Answer Any 5 Questions

Q.No		Max Marks	CO	BT/CL
1	<p><i>Illustrate the operational and non-operational quality attributes of an embedded systems.</i></p>	10	2	L2
2	<p><i>Design FSM model for tea/coffee vending machine.</i></p>	10	2	L2
3	<p><i>With the functional block diagram, explain the operation of Washing Machine as Application-Specific Embedded system</i></p>	10	1	L2

01:00 PM  
 02/11/20



4	Explain the different 'Embedded firmware design' approaches in detail.	10	2	L2
5	With neat diagram explain operating system architecture.	10	2	L2
6	Define process. Explain in detail the structure, memory organization and state transitions of the process.	10	2	L2



7	Explain Multiprocessing, Multitasking and Multiprogramming.	10	2	L2
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Evaluation

USN	Name	Present (P) / Absent (Ab)	Q1	Q2	Q3	Q4	Q5	Q6	Q7	IA Total	BT/CL
4AI19CS057	Koushik Lokesh K	P	10	10	9	9	8	0	0	46	Understand
4AI20CS003	Abhijna C D	P	10	10	10	10	10	0	0	50	Understand
4AI20CS005	Aishwaraya A S	P	10	10	10	6	4	0	0	40	Understand
4AI20CS006	Aisiri H T	P	10	10	10	9	10	0	0	49	Understand
4AI20CS007	Amith Shetty A R	P	4	10	10	0	0	0	0	24	Understand
4AI20CS008	Amulya J	P	10	10	8	10	10	0	0	48	Understand
4AI20CS010	Anil Kumar R Patil	P	10	10	10	0	10	0	0	40	Understand
4AI20CS011	Anjanni S Singh	P	10	10	10	0	10	0	8	48	Understand
4AI20CS012	Anvika B N	P	10	10	10	10	0	10	0	50	Understand
4AI20CS014	Arya K S	P	10	10	10	0	0	10	8	48	Understand
4AI20CS016	Bhakti Sampada J S	P	0	10	10	10	0	0	5	35	Understand
4AI20CS018	Bhoomika G V	P	10	0	10	10	10	8	0	48	Understand
4AI20CS022	Chandrashekhar M	P	9	10	9	10	8	0	10	48	Understand
4AI20CS023	Chethan K S	P	10	10	10	10	10	0	0	50	Understand
4AI20CS026	Daivik M D	P	10	10	10	8	9	0	0	47	Understand
4AI20CS027	Deeksha C T	P	10	10	10	0	0	10	10	50	Understand
4AI20CS028	Deekshith K	P	8	0	8	0	8	0	4	28	Understand
4AI20CS034	Gagana K R	P	10	10	10	10	10	0	0	50	Understand
4AI20CS035	Harshith M O	P	10	4	9	0	10	0	0	33	Understand
4AI20CS037	Harshitha K	P	10	10	10	4	10	2	0	44	Understand
4AI20CS038	Himasiri U A	P	9	10	10	4	7	0	0	40	Understand
4AI20CS040	Inchara B E	P	10	10	10	10	6	0	0	46	Understand
4AI20CS041	Jeevan D S	P	10	10	10	10	0	0	8	48	Understand

Subject Code  
 18XX81  
 10.00 AM to  
 12.00 PM to  
 01.00 PM  
 18XX82X

105



USN	Name	Present (P) / Absent (Ab)	Q1	Q2	Q3	Q4	Q5	Q6	Q7	IA Total	BT/CL
4AI20CS047	Kishan S Doddamani	P	10	0	4	0	5	0	4	23	Understand
4AI20CS048	Kubra Fathima	P	10	10	10	10	10	0	0	50	Understand
4AI20CS050	Likith M D	P	10	10	8	8	0	0	8	44	Understand
4AI20CS053	Manjunatha B	P	10	10	10	10	10	0	0	50	Understand
4AI20CS064	Nisarga S N	P	10	10	10	10	0	0	10	50	Understand
4AI20CS066	Pannaga B M	P	10	10	0	10	10	10	0	50	Understand
4AI20CS067	Pavan N	P	10	10	10	10	0	10	0	50	Understand
4AI20CS070	Preetham Gowda C B	P	9	10	10	6	0	0	0	35	Understand
4AI20CS071	Priya K M	P	10	0	10	10	10	10	0	50	Understand
4AI20CS072	Priyanka K	P	10	10	10	0	0	10	10	50	Understand
4AI20CS073	Rakshita	P	0	8	10	10	10	0	0	38	Understand
4AI20CS076	Ram Kumar G	P	10	10	10	10	0	0	2	42	Understand
4AI20CS078	Ratan Akki	P	10	10	10	10	10	0	0	50	Understand
4AI20CS080	Roopesh B Y	P	10	10	10	10	8	0	0	48	Understand
4AI20CS081	Saba Fathima	P	10	10	10	10	7	0	0	47	Understand
4AI20CS082	Sahana	P	8	10	10	6	10	0	0	44	Understand
4AI20CS084	Sahana R	P	10	10	10	0	0	10	10	50	Understand
4AI20CS086	Samanvaya K J	P	10	10	10	10	0	10	0	50	Understand
4AI20CS087	Sanath D U	P	10	10	10	0	0	0	0	30	Understand
4AI20CS089	Sanjana S	P	10	10	10	9	0	0	10	49	Understand
4AI20CS091	Sapnaz Banu I	P	10	10	10	10	10	0	0	50	Understand
4AI20CS093	Savitra Shenoy	P	10	10	10	10	10	10	0	50	Understand
4AI20CS095	Shishira B J	P	7	8	8	9	0	10	8	43	Understand
4AI20CS097	Shreehari S Rao	P	7	8	10	0	2	0	4	31	Understand
4AI20CS101	Sinchana B R	P	10	10	10	10	7	0	0	47	Understand
4AI20CS103	Sridevi K S	P	0	0	0	0	0	0	0	50	No Level
4AI20CS105	Sudhanva A P	P	10	10	10	8	7	0	0	45	Understand
4AI20CS107	Suhas	P	10	10	10	10	0	10	0	50	Understand
4AI20CS109	Sujay G	P	10	10	6	0	0	8	4	38	Understand
4AI20CS111	Sumedh M Anvekar	P	10	10	10	10	0	10	0	50	Understand
4AI20CS114	Tanushree D	P	10	10	10	0	4	2	0	36	Understand
4AI20CS115	Uday Rao C G	P	10	10	10	0	8	0	5	43	Understand
4AI20CS116	Vamshika M J	P	10	10	10	0	8	10	0	48	Understand
4AI20CS117	Varsha B C	P	10	10	10	10	10	10	0	50	Understand
4AI20CS118	Varsha V	P	10	10	10	10	6	0	0	46	Understand
4AI20CS119	Varshini G	P	10	8	10	8	0	0	2	38	Understand
4AI20CS120	Vidya N G	P	7	10	10	5	0	0	4	36	Understand





# ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY

Department of Computer Science and Engineering (CS)

USN	Name	Present (P) / Absent (Ab)	Q1	Q2	Q3	Q4	Q5	Q6	Q7	IA Total	BT/CL
4AI20CS121	Vijayalakshmi G S	P	10	10	10	10	10	0	0	50	Understand
4AI20CS124	Vivek B R	P	9	10	10	8	2	0	0	39	Understand
4AI20CS125	Yashaswini R	P	10	10	10	0	10	10	0	50	Understand
4AI20CS127	Bhoomika S P	P	7	10	10	0	10	5	0	42	Understand
4AI21CS401	Manoj Kumar U	P	10	10	10	0	0	0	6	36	Understand
4AI21CS403	Mohan D S	P	10	8	10	10	6	0	0	44	Understand
4AI21CS405	Ranjita Laxman Naik	P	10	10	10	9	5	2	0	44	Understand
4AI21CS407	Sanjay K Y	P	10	10	10	8	0	8	0	46	Understand
4AI21CS410	Vinay M N	P	10	10	10	8	10	0	0	48	Understand



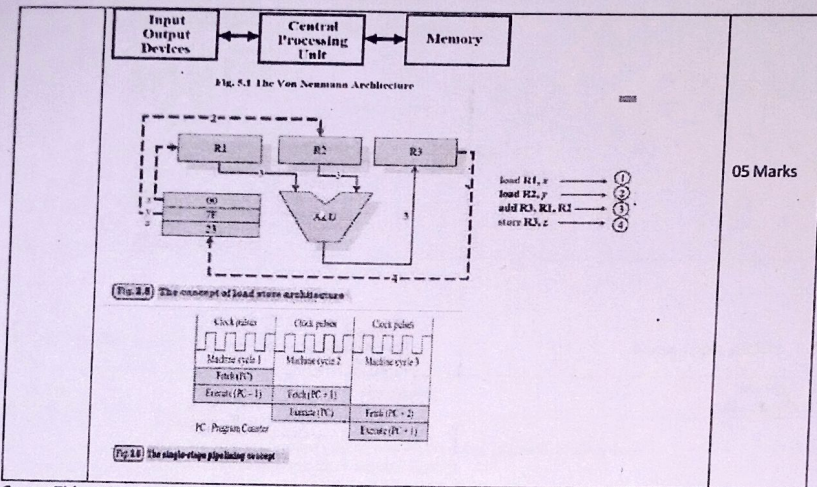
### 2 Scheme of Evaluation

#### SCHEME AND SOLUTIONS

Course Title: MicroController and Embedded System

Course Code: 18CS44

Q. No.	SOLUTION	MARKS ALLOCATED																		
1	<p><b>Single-Register Transfer</b></p> <ul style="list-style-type: none"> <li>Address accessed by LDR/STR is specified by a base register with an offset</li> <li>For word and unsigned byte accesses, offset can be:               <ul style="list-style-type: none"> <li>An unsigned 12-bit immediate value (i.e. 0 - 4095 bytes)</li> </ul> </li> </ul> <p>LDR r0, [r1, #8]            - A register, optionally shifted by an immediate value</p> <p>LDR r0, [r1, r2]            LDR r0, [r1, r2, LSL#2]</p> <ul style="list-style-type: none"> <li>This can be either added or subtracted from the base register:</li> </ul> <p>LDR r0, [r1, #-8]            LDR r0, [r1, -r2, LSL#2]</p> <ul style="list-style-type: none"> <li>For halfword and signed halfword / byte, offset can be:               <ul style="list-style-type: none"> <li>An unsigned 8 bit immediate value (i.e. 0 - 255 bytes)</li> <li>A register (unshifted)</li> </ul> </li> <li>Choice of <i>pre-indexed</i> or <i>post-indexed</i> addressing</li> <li>Choice of whether to update the base pointer (pre-indexed only)</li> </ul> <p>LDR r0, [r1, #-8]!</p> <p><b>Index methods.</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>Index method</th> <th>Data</th> <th>Base address register</th> <th>Example</th> </tr> </thead> <tbody> <tr> <td>Preindex with writeback</td> <td>mem[base + offset]</td> <td>base + offset</td> <td>LDR r0, [r1, #4]!</td> </tr> <tr> <td>Preindex</td> <td>mem[base + offset]</td> <td>not updated</td> <td>LDR r0, [r1, #4]</td> </tr> <tr> <td>Postindex</td> <td>mem[base]</td> <td>base + offset</td> <td>LDR r0, [r1], #4</td> </tr> </tbody> </table>	Index method	Data	Base address register	Example	Preindex with writeback	mem[base + offset]	base + offset	LDR r0, [r1, #4]!	Preindex	mem[base + offset]	not updated	LDR r0, [r1, #4]	Postindex	mem[base]	base + offset	LDR r0, [r1], #4	<p>05Marks</p> <p>05Marks</p>		
Index method	Data	Base address register	Example																	
Preindex with writeback	mem[base + offset]	base + offset	LDR r0, [r1, #4]!																	
Preindex	mem[base + offset]	not updated	LDR r0, [r1, #4]																	
Postindex	mem[base]	base + offset	LDR r0, [r1], #4																	
2.	<p>Syntax: SWP(B) {&lt;cond&gt;} Rd, Rm, [Rn]</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tbody> <tr> <td style="width: 10%;">SWP</td> <td style="width: 50%;">swap a word between memory and a register</td> <td style="width: 40%;"> <math>tmp = mem32[Rn]</math>  <math>mem32[Rm] = Rm</math>  <math>Rd = tmp</math> </td> </tr> <tr> <td>SWPB</td> <td>swap a byte between memory and a register</td> <td> <math>tmp = mem8[Rm]</math>  <math>mem8[Rn] = Rm</math>  <math>Rd = tmp</math> </td> </tr> </tbody> </table> <p>Syntax: B{&lt;cond&gt;} label            BL{&lt;cond&gt;} label            BX{&lt;cond&gt;} Rm            BLX{&lt;cond&gt;} label   Rm</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tbody> <tr> <td style="width: 10%;">B</td> <td style="width: 40%;">branch</td> <td style="width: 50%;">pc = label</td> </tr> <tr> <td>DL</td> <td>branch with link</td> <td>pc = label lr = address of the next instruction after the BL</td> </tr> <tr> <td>BX</td> <td>branch exchange</td> <td><math>\mu = Rm \ \&amp; \ 0x1FFFFFFF, T = Rm \ \&amp; \ 1</math></td> </tr> <tr> <td>BLX</td> <td>branch exchange with link</td> <td>pc = label, T = 1 <math>\mu = Rm \ \&amp; \ 0x1FFFFFFF, T = Rm \ \&amp; \ 1</math> lr = address of the next instruction after the BLX</td> </tr> </tbody> </table>	SWP	swap a word between memory and a register	$tmp = mem32[Rn]$ $mem32[Rm] = Rm$ $Rd = tmp$	SWPB	swap a byte between memory and a register	$tmp = mem8[Rm]$ $mem8[Rn] = Rm$ $Rd = tmp$	B	branch	pc = label	DL	branch with link	pc = label lr = address of the next instruction after the BL	BX	branch exchange	$\mu = Rm \ \& \ 0x1FFFFFFF, T = Rm \ \& \ 1$	BLX	branch exchange with link	pc = label, T = 1 $\mu = Rm \ \& \ 0x1FFFFFFF, T = Rm \ \& \ 1$ lr = address of the next instruction after the BLX	<p>05Marks</p> <p>05 Marks</p>
SWP	swap a word between memory and a register	$tmp = mem32[Rn]$ $mem32[Rm] = Rm$ $Rd = tmp$																		
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DL	branch with link	pc = label lr = address of the next instruction after the BL																		
BX	branch exchange	$\mu = Rm \ \& \ 0x1FFFFFFF, T = Rm \ \& \ 1$																		
BLX	branch exchange with link	pc = label, T = 1 $\mu = Rm \ \& \ 0x1FFFFFFF, T = Rm \ \& \ 1$ lr = address of the next instruction after the BLX																		
3.	<p><b>Harvard vs. Von-neumann Processor/Controller:</b></p>	<p>05 Marks</p>																		

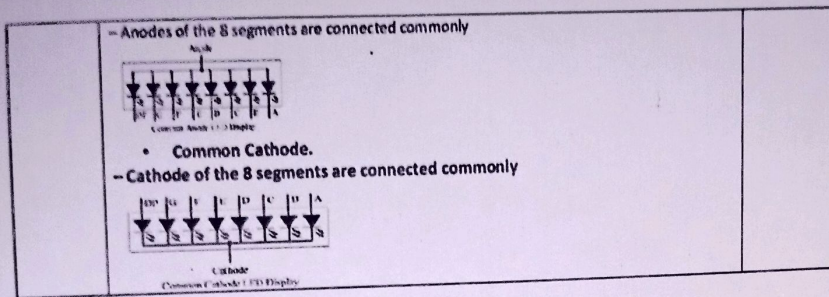


05 Marks

Course Title: MicroController and Embedded System

Course Code: 18CS44

Q. No.	SOLUTION	MARKS ALLOCATED
4.	<p>1. Types of Memory in Embedded System:</p> <p>A) Program Storage Memory (ROM)</p> <p>b) Masked ROM (MROM)</p> <p>c) Programmable Read Only Memory (PROM)/ (OTP)</p> <p>d) Erasable Programmable Read Only Memory (EPROM)</p> <p>e) Electrically Erasable Programmable Read Only Memory (EEPROM)</p> <p>f) FLASH</p> <p>2. Read-Write Memory/Random Access Memory (RAM)</p> <p>a) Static RAM (SRAM)</p> <p>b) Dynamic RAM (DRAM)</p> <p>c) Non-volatile RAM(NVRAM)</p>	<p>05 Marks</p> <p>05 Marks</p>
5.	<p>7-segment LED display:</p> <ul style="list-style-type: none"> <li>Is an output device for displaying alphanumeric characters.</li> <li>It contains 8 LED segments arranged in a special form.</li> <li>Out of the 8 LED segments               <ul style="list-style-type: none"> <li>7 are used for displaying alphanumeric characters and are named as A-G</li> <li>1 is used for representing 'decimal point' in decimal number display.</li> </ul> </li> </ul> <p>Common Anode</p>	05 Mark

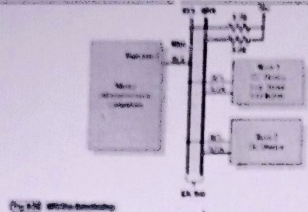
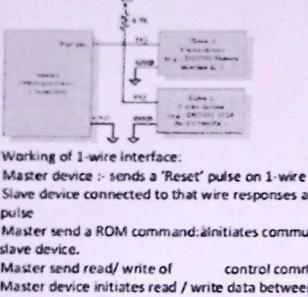
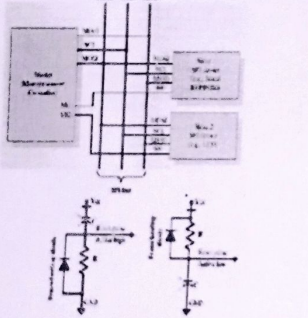
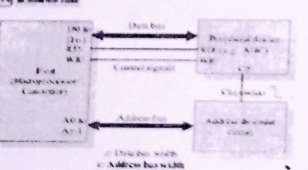


Course Title: Micro Controller and Embedded System

Course Code: 18CS44

Q. No.	SOLUTION	MARKS ALLOCATED
	<p>A stepper motor:</p> <ul style="list-style-type: none"> <li>• Is an electro-mechanical device which generates discrete displacement (motion) in response to dc electrical signals.</li> <li>• It differs from the normal dc motor in its operation.               <ul style="list-style-type: none"> <li>- DC motor produces continuous rotation on applying dc voltage</li> <li>- Stepper motor produces discrete rotation in response to the dc voltage applied to it.</li> </ul> </li> <li>• The paper feed mechanism of a printer/fax makes use of stepper motors for its functioning.</li> </ul> <p>Fig. 2.10 3-Phase multipole step motor.</p>	05 Marks
6	<p>Inter Integrated Circuit (I<sup>2</sup>C) Bus:</p> <p>2 Buses: SCL, SDA          2 Devices: Master &amp; Slave          Master Device: For controlling</p> <ul style="list-style-type: none"> <li>• Initiating/terminating data transfer</li> <li>• Sending data</li> <li>• Generating necessary synchronization clock pulses.</li> </ul> <p>Slave Device:</p> <ul style="list-style-type: none"> <li>• Wait for the commands from the master</li> <li>• Respond upon receiving the commands.</li> </ul>	05 Marks



	 <p>Fig 1.10 1-wire bus interface</p>	05 Marks
7	<p>Working of 1-wire interface:</p> <ol style="list-style-type: none"> <li>1. Master device - sends a 'Reset' pulse on 1-wire bus.</li> <li>2. Slave device connected to that wire responds as 'Present' pulse</li> <li>3. Master send a ROM command: initiates communication at slave device.</li> <li>4. Master send read/ write of control command</li> <li>5. Master device initiates read / write data between devices</li> </ol>  <p>Fig 1.11 1-wire bus interface</p>  <p>Fig 1.12 1-wire bus interface</p>  <p>Fig 1.13 Bus interface</p>	05 Marks

Internal Test Schedule for eighth Semester

1<sup>st</sup> Test Cycle

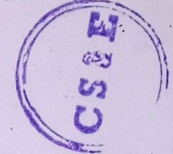
DATE	21-05-2022	21-05-2022
DAY	Saturday	Saturday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM
Subject Code	18XX81	18XX82X

2<sup>nd</sup> Test Cycle

DATE	10-06-2022	10-06-2022
DAY	Friday	Friday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM
Subject Code	18XX81	18XX82X

3<sup>rd</sup> Test Cycle

DATE	30-06-2022	30-06-2022
DAY	Thursday	Thursday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM
Subject Code	18XX81	18XX82X



*Handwritten initials/signature*

TELL Co-ordinator: *Handwritten signature*

*Handwritten signature*  
**Dr. C. T. JAYADEVA**  
 Principal B.E., M. Tech., Ph.D.  
 Adichunchanagiri Institute of Technology  
 CHIKKAMAGALURU-577102

## Internal Test Schedule for Sixth Semester

1<sup>st</sup> Test Cycle

DATE	27-05-2022	27-05-2022	28-05-2022	28-05-2022	29-05-2022
DAY	Friday	Friday	Saturday	Saturday	Sunday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM
Subject Code	18XX61	18XX62	18XX63	18XX64X	18XX65X

2<sup>nd</sup> Test Cycle

DATE	25-06-2022	25-06-2022	26-06-2022	26-06-2022	27-06-2022
DAY	Saturday	Saturday	Sunday	Sunday	Monday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM
Subject Code	18XX61	18XX62	18XX63	18XX64X	18XX65X

3<sup>rd</sup> Test Cycle

DATE	14-07-2022	14-07-2022	15-07-2022	15-07-2022	16-07-2022
DAY	Thursday	Thursday	Friday	Friday	Saturday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM
Subject Code	18XX61	18XX62	18XX63	18XX64X	18XX65X

Test Coordinator: Mr RSM

Dr. C. T. JAYADEVA  
Principal  
Adichunchanagiri Institute of Technol  
CHIKKAMAGALURU-577102



||Jai Sri Gurudev ||

Adichunchanagiri Shikshana Trust (R)

Adichunchanagiri Institute of Technology, Chikmagalur - 577102

Internal Test Schedule for Fourth Semester



Date: 02-06-2022

1<sup>st</sup> Test Cycle

DATE	01-07-2022	01-07-2022	01-07-2022	02-07-2022	02-07-2022	03-07-2022	03-07-2022
DAY	Friday	Friday	Friday	Saturday	Saturday	Sunday	Sunday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	03.30 PM to 04.30 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM
Subject Code	18MAT41	18XX42	18KVK/KAK/CPC 49	18XX43	18XX44	18XX45	18XX46

2<sup>nd</sup> Test Cycle

DATE	31-07-2022	31-07-2022	01-08-2022	01-08-2022	02-08-2022	02-08-2022	02-08-2022
DAY	Sunday	Sunday	Monday	Monday	Tuesday	Tuesday	Tuesday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	03.30 PM to 04.30 PM
Subject Code	18MAT41	18XX42	18XX43	18XX44	18XX45	18XX46	18KVK/KAK/CPC 49

3<sup>rd</sup> Test Cycle

DATE	27-08-2022	27-08-2022	28-08-2022	28-08-2022	29-08-2022	29-08-2022	29-08-2022
DAY	Saturday	Saturday	Sunday	Sunday	Monday	Monday	Monday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	03.30 PM to 04.30 PM
Subject Code	18MAT41	18XX42	18XX43	18XX44	18XX45	18XX46	18KVK/KAK/CPC 49

*Dr. C. T. Jayadeva*

**Dr. C. T. JAYADEVA**

Principal B.E., M.Tech., Ph.D.

Adichunchanagiri Institute of Technology  
CHIKKAMAGALURU-577102

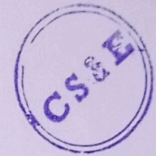


||Jai Sri Gurudev ||

Adichunchanagiri Shikshana Trust (R)

Adichunchanagiri Institute of Technology, Chikmagalur - 577102

Internal Test Schedule for Second Semester Physics Cycle



Date: 09-06-2022

1<sup>st</sup> Test Cycle

DATE	01-07-2022	01-07-2022	02-07-2022	02-07-2022
DAY	Friday	Friday	Saturday	Saturday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM
Subject Code	21MAT21	21PHY22	21ELE23	21CIV24

2<sup>nd</sup> Test Cycle

DATE	31-07-2022	31-07-2022	01-08-2022	01-08-2022
DAY	Sunday	Sunday	Monday	Monday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM
Subject Code	21MAT21	21PHY22	21ELE23	21CIV24

3<sup>rd</sup> Test Cycle

DATE	27-08-2022	27-08-2022	28-08-2022	28-08-2022
DAY	Saturday	Saturday	Sunday	Sunday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM
Subject Code	21MAT21	21PHY22	21ELE23	21CIV24

*C. T. Jayadeva*  
**Dr. C. T. JAYADEVA**  
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||Jai Sri Gurudev ||

Adichunchanagiri Shikshana Trust (R)

# Adichunchanagiri Institute of Technology, Chikmagalur - 577102

## Internal Test Schedule for Second Semester Chemistry Cycle

Date: 09-06-2022

### 1<sup>st</sup> Test Cycle

DATE	01-07-2022	01-07-2022	02-07-2022	02-07-2022	03-07-2022
DAY	Friday	Friday	Saturday	Saturday	Sunday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM
Subject Code	21MAT21	21PSP23	21CHE22	21ELN24	21EME25

### 2<sup>nd</sup> Test Cycle

DATE	31-07-2022	31-07-2022	01-08-2022	01-08-2022	02-08-2022
DAY	Sunday	Sunday	Monday	Monday	Tuesday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM
Subject Code	21MAT21	21PSP23	21CHE22	21ELN24	21EME25

### 3<sup>rd</sup> Test Cycle

DATE	27-08-2022	27-08-2022	28-08-2022	28-08-2022	29-08-2022
DAY	Saturday	Saturday	Sunday	Sunday	Monday
TIME	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM	12.00 PM to 01.00 PM	09.00 AM to 10.00 AM
Subject Code	21MAT21	21PSP23	21CHE22	21ELN24	21EME25

*Dr. C. T. Jayadeva*  
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